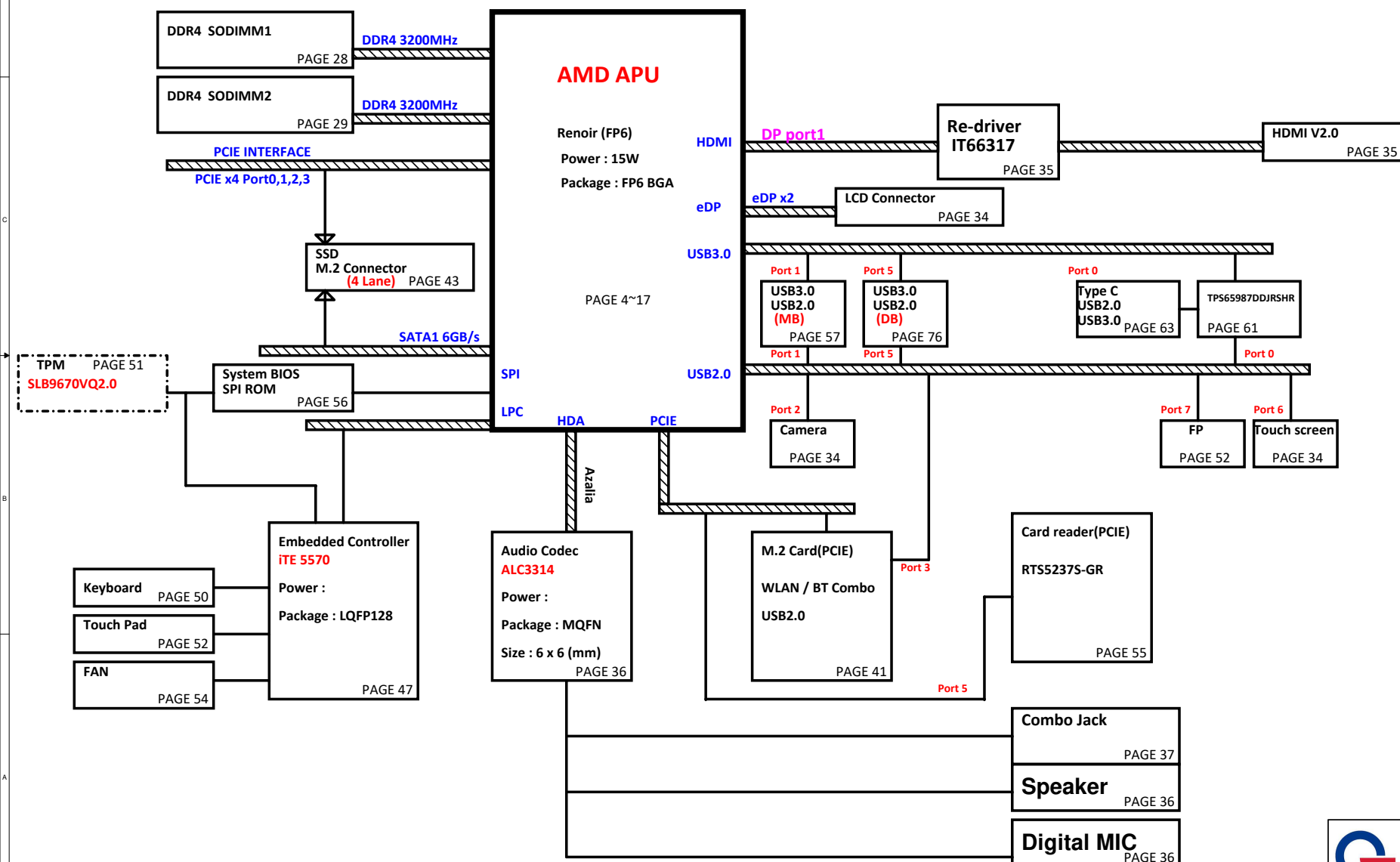


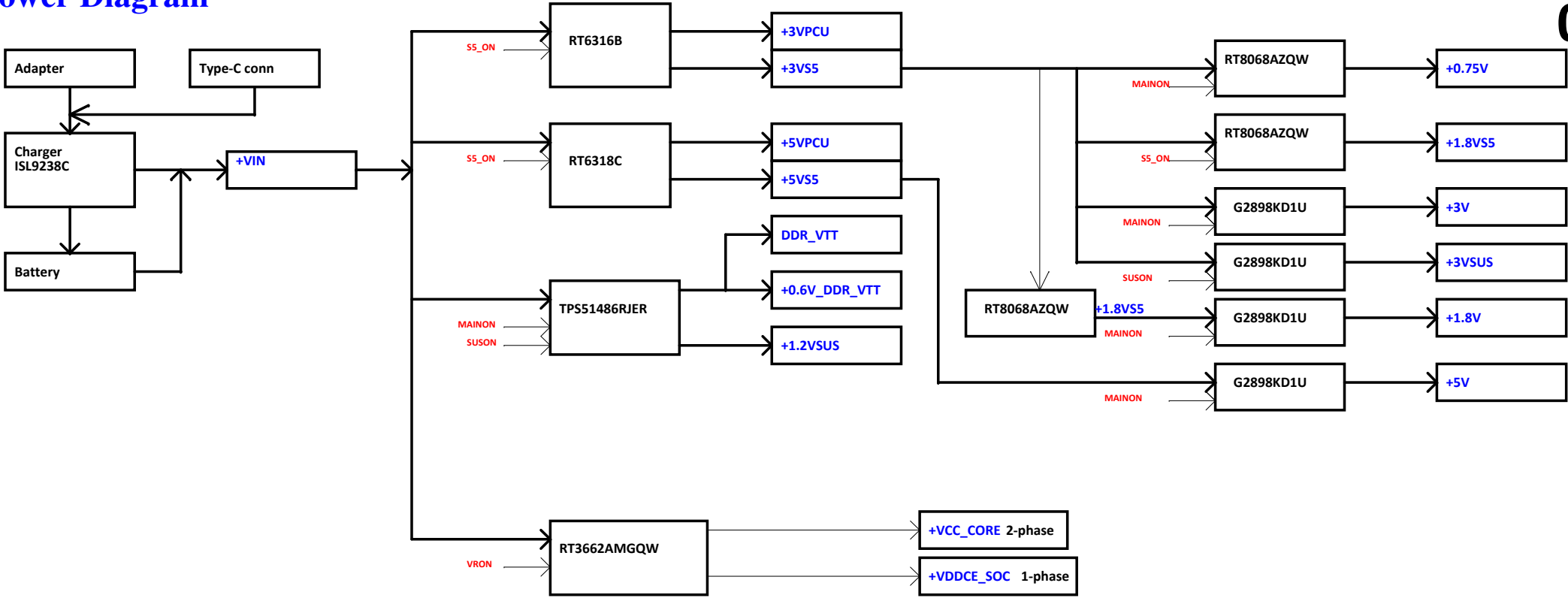
Space Animal 15.6" AMD Renoir Ridge UMA

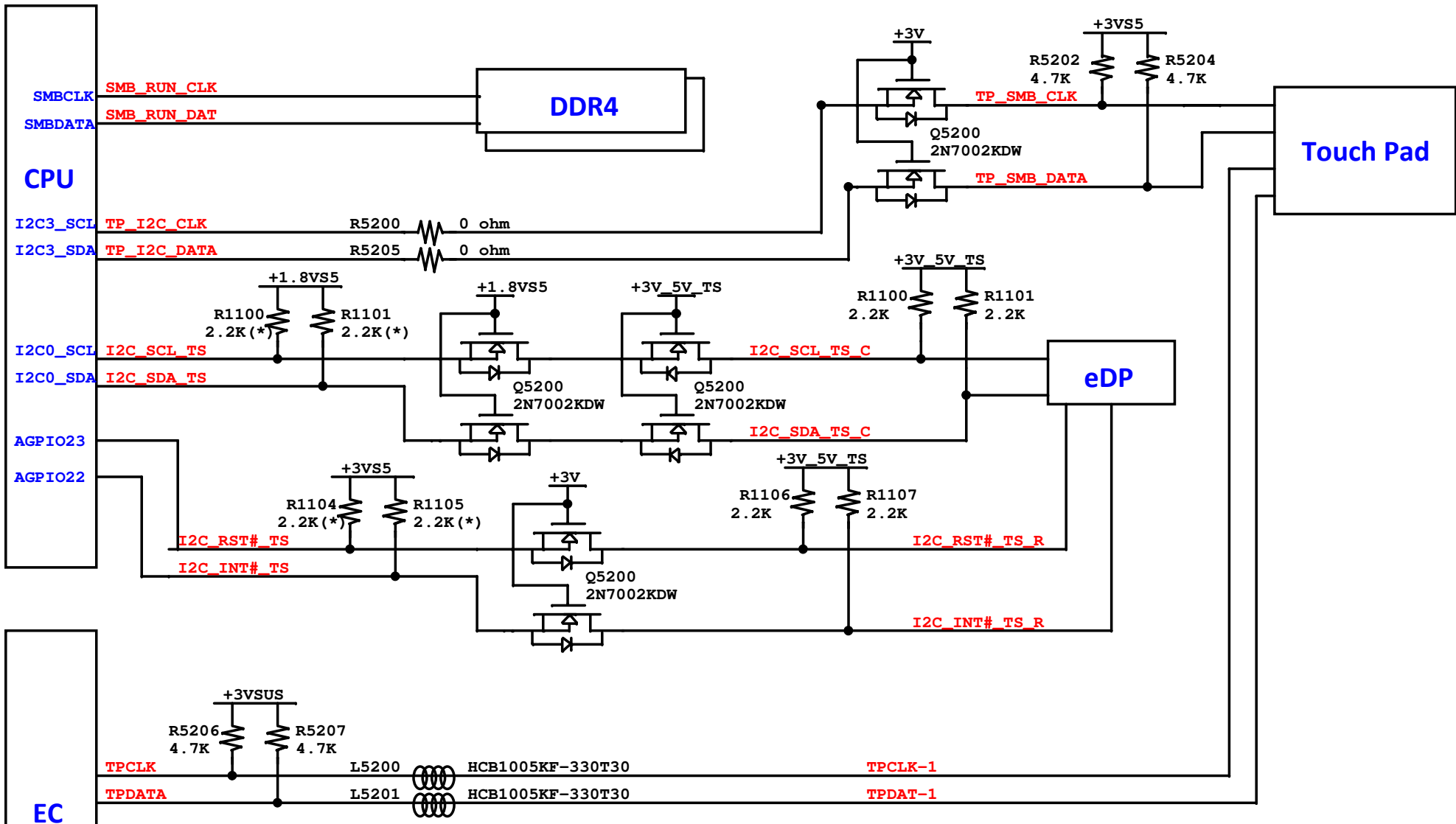
PCB 8L STACK UP

LAYER 1 : TOP
LAYER 2 : GND
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : SVCC
LAYER 6 : IN3
LAYER 7 : GND
LAYER 8 : BOT



Power Diagram





PCIe Port	Function
GFX 0~8	NC
PCIe 3~0	SSD1(PCIe/SATA)
PCIe 4	NC
PCIe 5	CR
PCIe 6	NC
PCIe 7	WLAN
PCIe 8~11	NC

AMD APU	QPN (PR)	QPN (PC)
Ryzen 7-4700U	AJ00100UT34	AJ00100UT12
Ryzen 5-4500U	AJ00100UT33	AJ00100UT06
Ryzen 3-4300U	AJ00100ST04	AJ00100UT07

PCIe-SSD 4 lane
(Port3 --> Port0)

SSD SATA/PCIe exchange port

CARD

WLAN

20191115
support MS change from 6 to 7

PCIe-SSD 4 lane
(Port3 --> Port0)

SSD SATA/PCIe exchange port

CARD

WLAN

U1B
PCIe

G13 P_GFX_RXP0
F13 P_GFX_RXN0
J14 P_GFX_RXP1
H14 P_GFX_RXN1
G15 P_GFX_RXP2
F15 P_GFX_RXN2
J15 P_GFX_RXP3
K15 P_GFX_RXN3
H16 P_GFX_RXP4
J16 P_GFX_RXN4
F18 P_GFX_RXP5
G18 P_GFX_RXN5
J18 P_GFX_RXP6
K18 P_GFX_RXN6
H19 P_GFX_RXP7
G19 P_GFX_RXN7

P_GFX_TXP0
P_GFX_TXN0
P_GFX_TXP1
P_GFX_TXN1
P_GFX_TXP2
P_GFX_TXN2
P_GFX_TXP3
P_GFX_TXN3
P_GFX_TXP4
P_GFX_TXN4
P_GFX_TXP5
P_GFX_TXN5
P_GFX_TXP6
P_GFX_TXN6
P_GFX_TXP7
P_GFX_TXN7

F4
F2
F3
E4
E1
C1
D5
E6
C6
D6
B6
C7
D8
B8
C8
A8

G11 P_GPP_RXP0
F11 P_GPP_RXN0
J10 P_GPP_RXP1
H10 P_GPP_RXN1
G8 P_GPP_RXP2/SATA0_RXP
F8 P_GPP_RXN2/SATA0_RXN
G6 P_GPP_RXP3/SATA1_RXP
F7 P_GPP_RXN3/SATA1_RXN

P_GPP_TXP0
P_GPP_TXN0
P_GPP_TXP1
P_GPP_TXN1
P_GPP_TXP2/SATA0_TXP
P_GPP_TXN2/SATA0_TXN
P_GPP_TXP3/SATA1_TXP
P_GPP_TXN3/SATA1_TXN

M9 P_GPP_RXP4
L6 P_GPP_RXP5
L7 P_GPP_RXP6
K7 P_GPP_RXP7
K6 P_GPP_RXN6
H6 P_GPP_RXN7
H7 P_GPP_RXN7

P_GPP_TXP4
P_GPP_TXN4
P_GPP_TXP5
P_GPP_TXN5
P_GPP_TXP6
P_GPP_TXN6
P_GPP_TXP7
P_GPP_TXN7

L9 P_GPP_RXP8/SATA2_RXP
L10 P_GPP_RXN8/SATA2_RXN
K11 P_GPP_RXP9/SATA3_RXP
J11 P_GPP_RXN9/SATA3_RXN
J12 P_GPP_RXP10
H12 P_GPP_RXN10
J13 P_GPP_RXP11
K13 P_GPP_RXN11

P_GPP_TXP8/SATA2_TXP
P_GPP_TXN8/SATA2_TXN
P_GPP_TXP9/SATA3_TXP
P_GPP_TXN9/SATA3_TXN
P_GPP_TXP10
P_GPP_TXN10
P_GPP_TXP11
P_GPP_TXN11

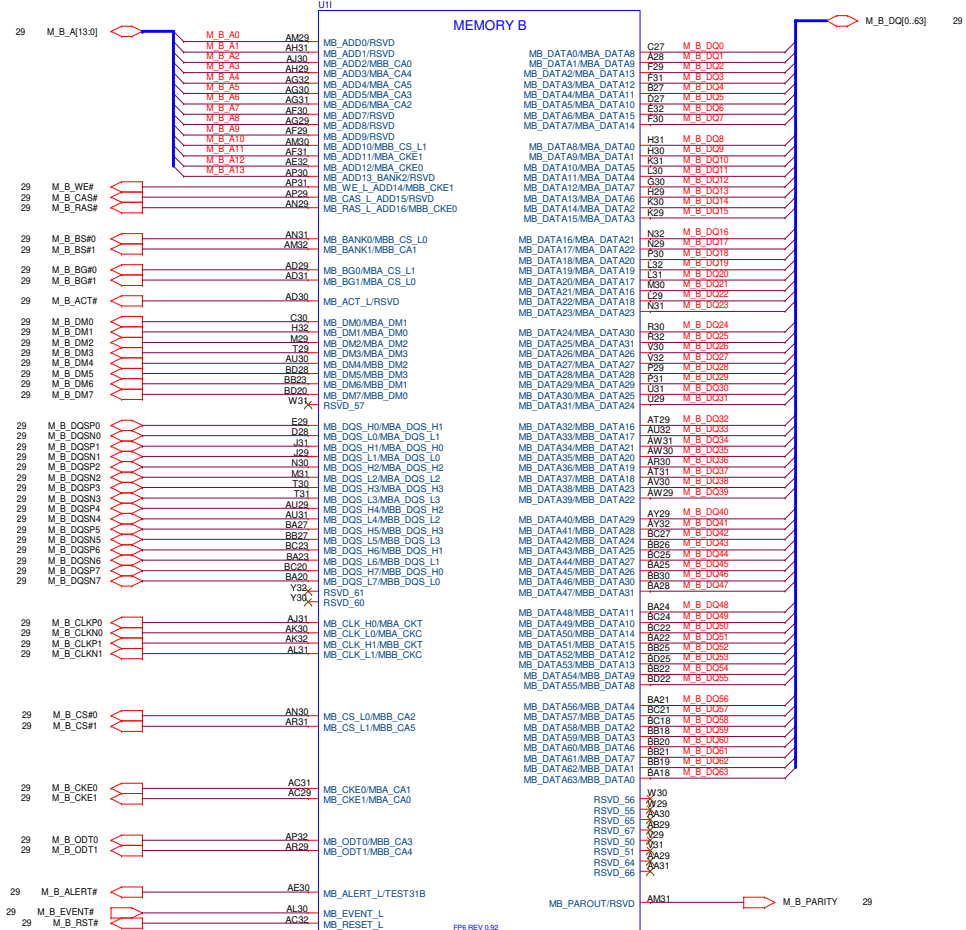
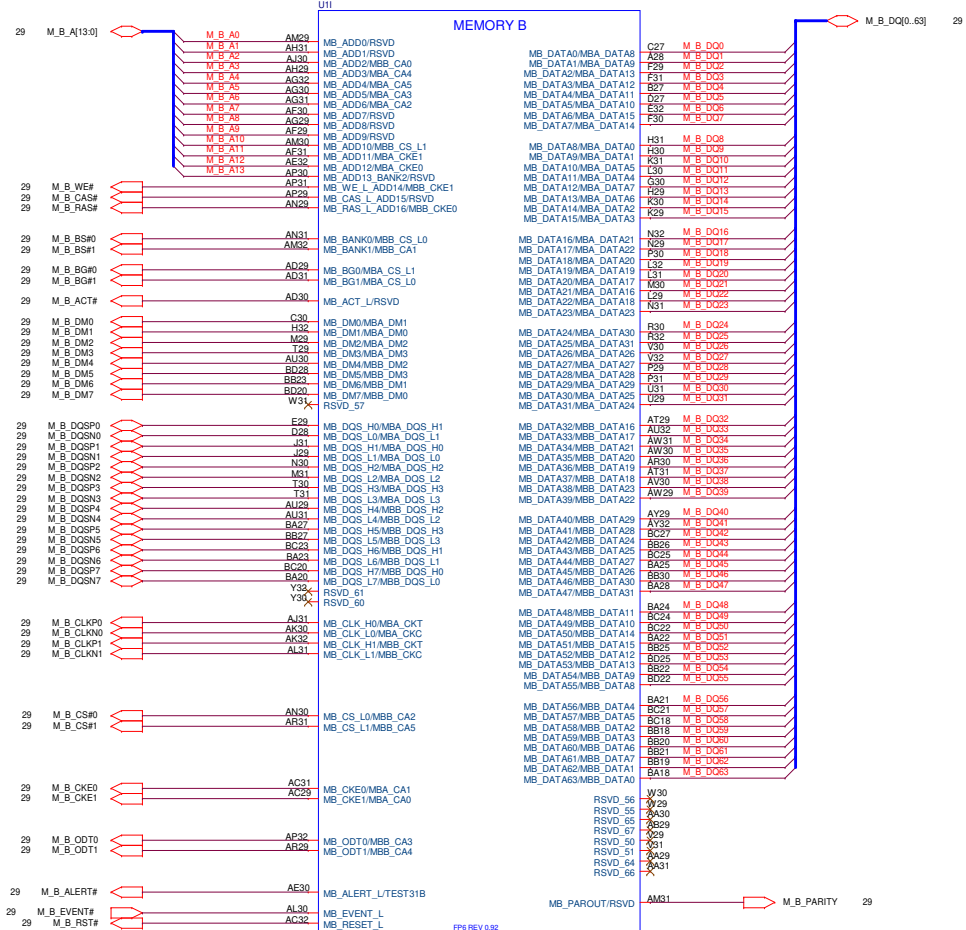
FP6 REV 0.92
PART 2 OF 13

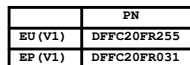
*CPU_AMD_FP6



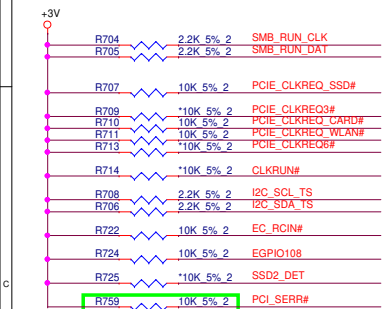
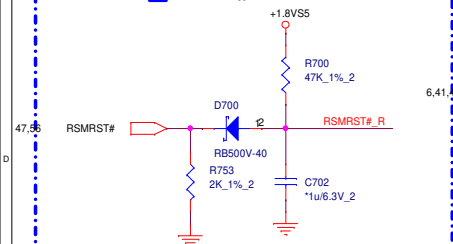
PROJECT : G7HA
Quanta Computer Inc.

Size Custom	Document Number RENOIR 1/7(PCIE)	Rev 1A
Date: Friday, July 10, 2020	Sheet 4 of 106	

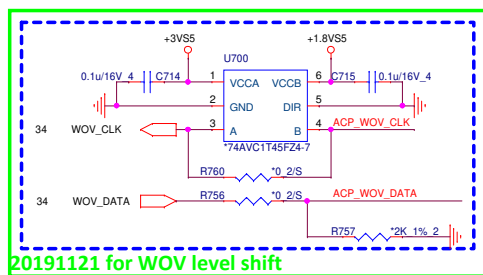




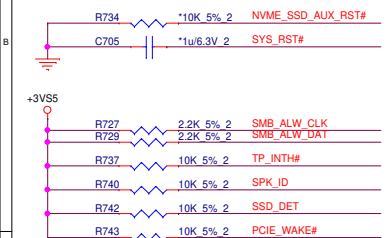
RSMRST_GATE# from EC



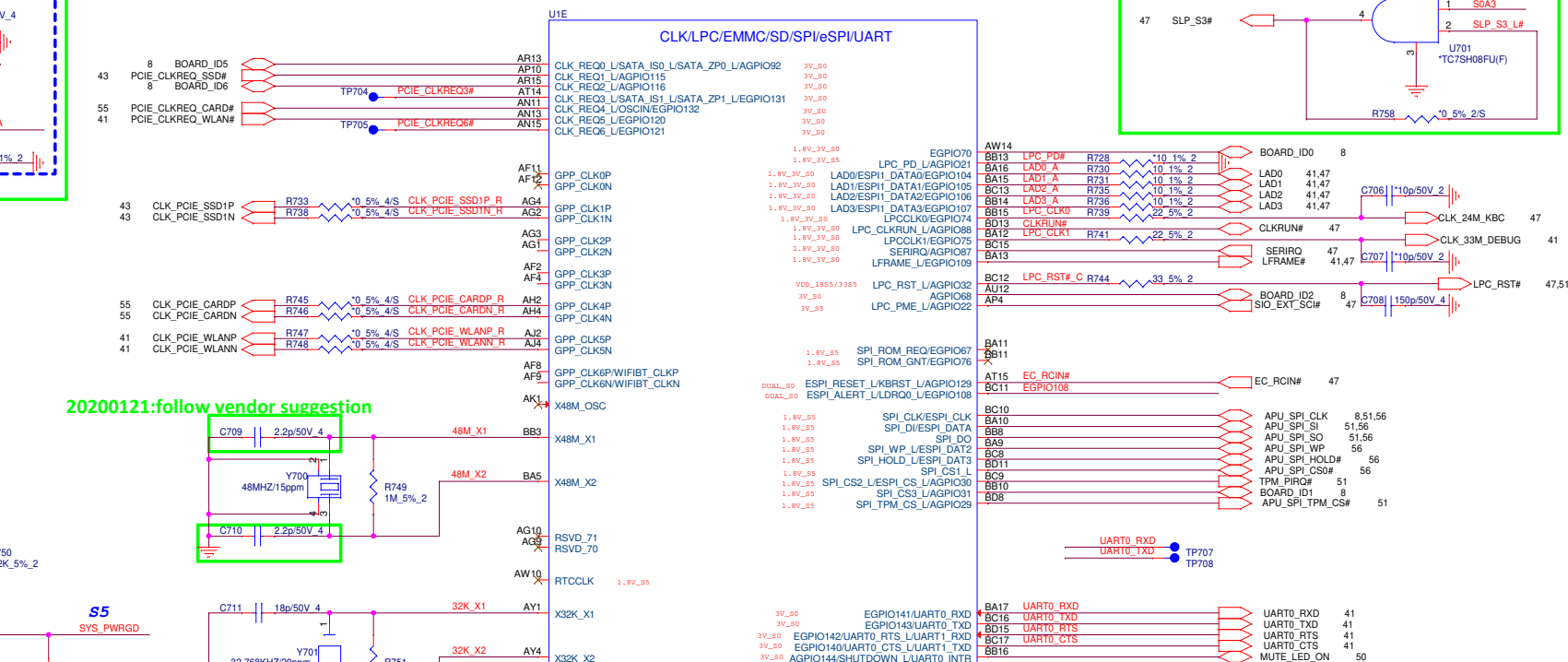
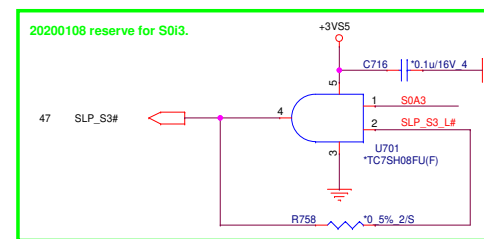
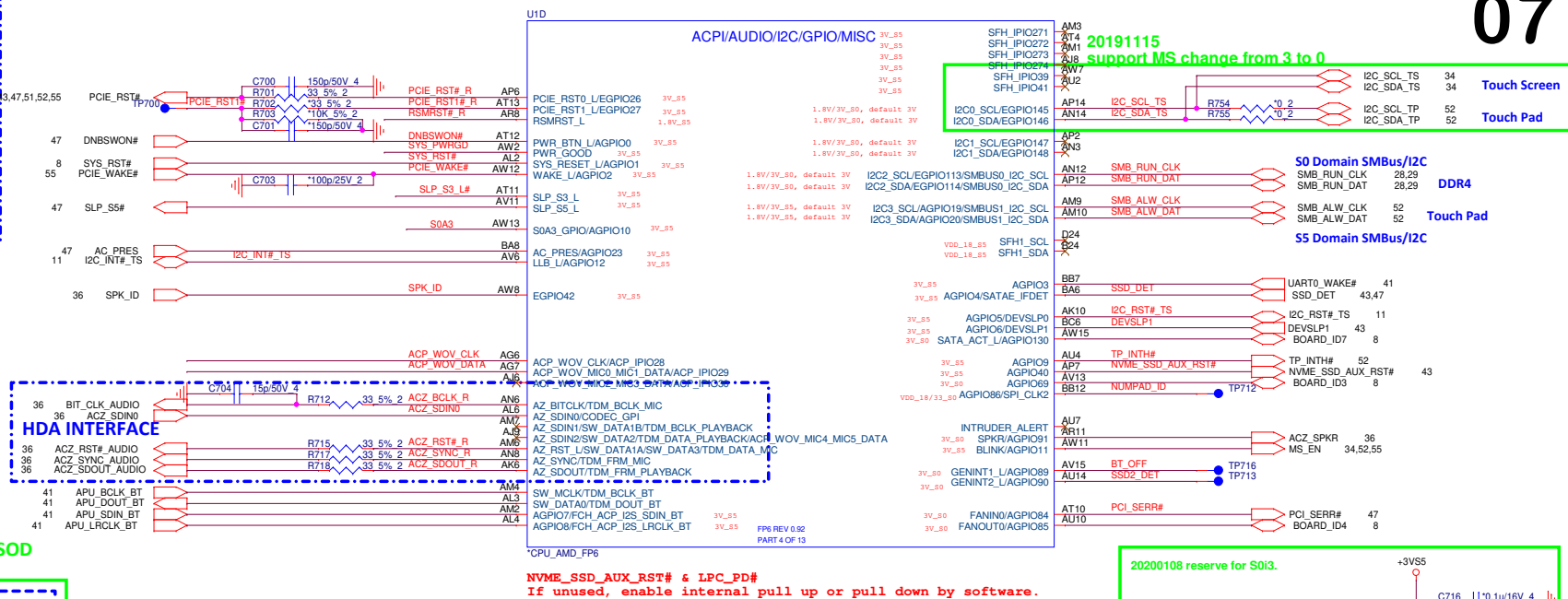
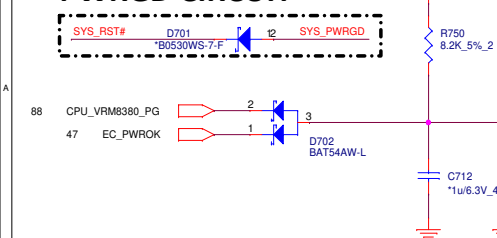
0122 : avoid trigger BSOD

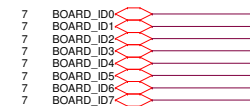
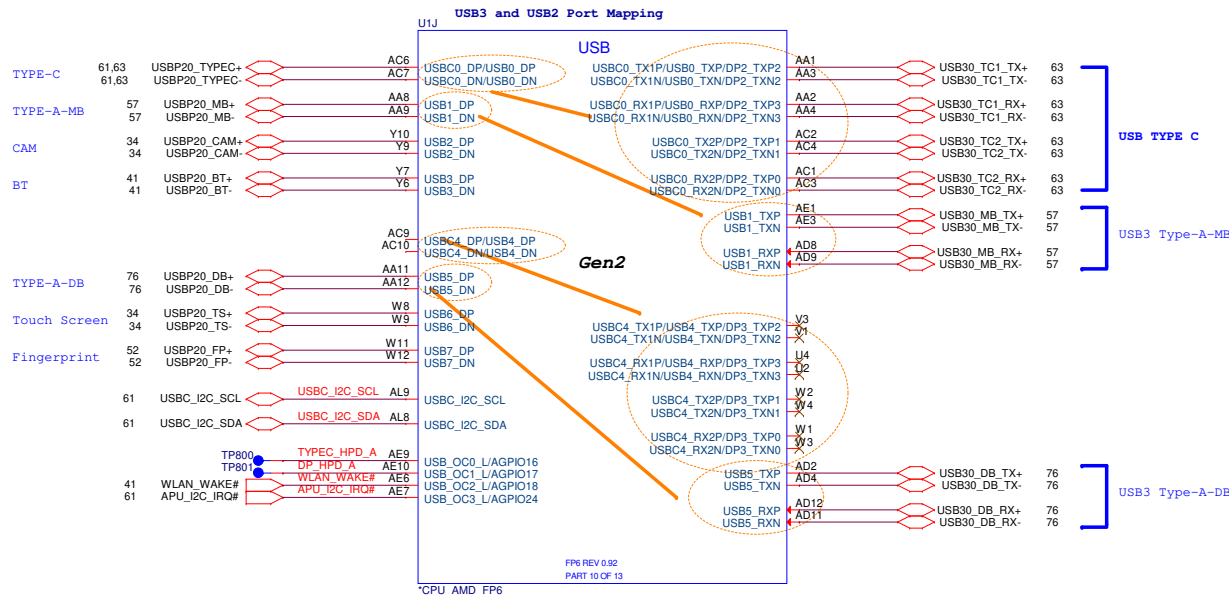


20191121 for WOV level shift



PWRGD CIRCUIT





BOARD ID SETTING

Board ID 0	Definition
0	UMA
1	DIS

Board ID [2:1]	Definition
00	13"
01	14"
10	15"
11	RVS

Board ID 3	Definition
0	2G
1	4G

Board ID 4	Definition
0	I2C Type for TP
1	USB Type for TP

Board ID [5]	Definition
0	None TPM
1	TPM

Board ID [6]	Definition
0	Non Touch
1	Touch (Default)

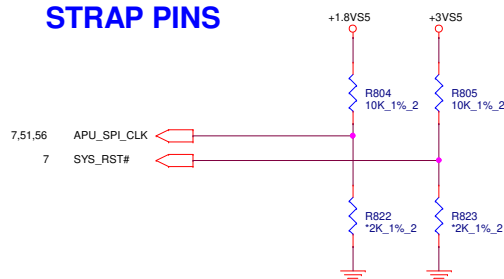
Board ID [7]	Definition
0	I2C Type for TS
1	USB Type for TS



USB2 Port	Function
USB0	TYPE-C
USB1	TYPE-A-MB
USB2	CAM
USB3	BT
USB4	
USB5	TYPE-A-DB
USB6	Touch screen
USB7	Finger print

USB3 Port	Function
USB0	TYPE-C
USB1	TYPE-A-MB
USB4	
USB5	TYPE-A-DB

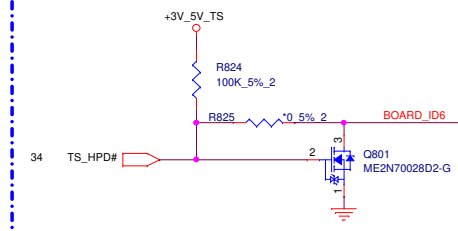
STRAP PINS



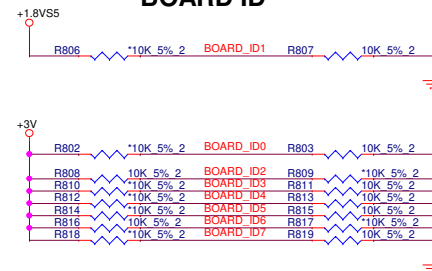
REQUIRED STRAPS

	APU_SPI_CLK	SYS_RST#
PULL HIGH	Use 48Mhz crystal clock and generate both internal and external clocks DEFAULT	Normal reset mode DEFAULT
PULL LOW	Use 100Mhz PCIE clock as reference clock and generate internal clocks only	Short reset mode

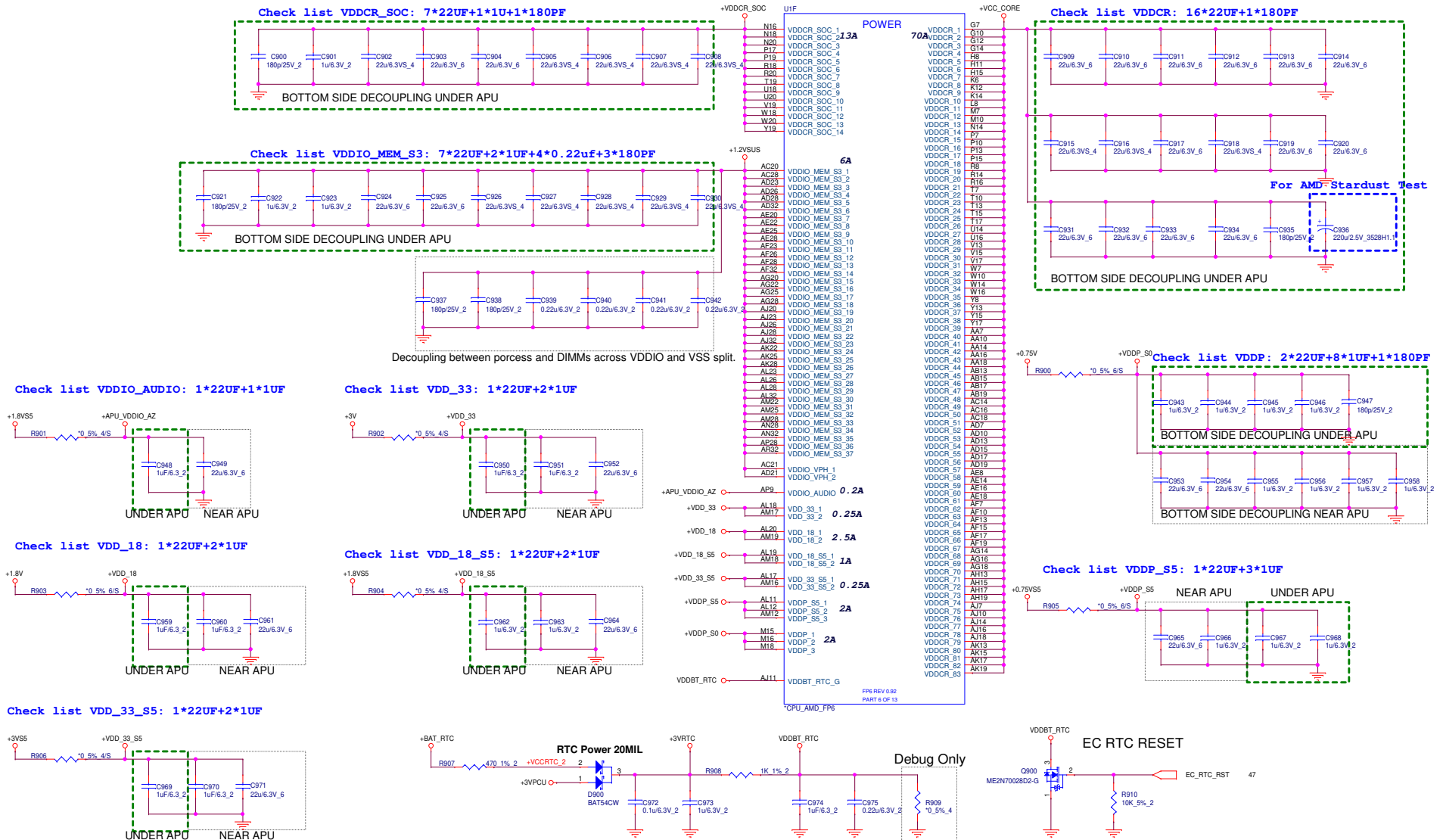
for T/S Determination

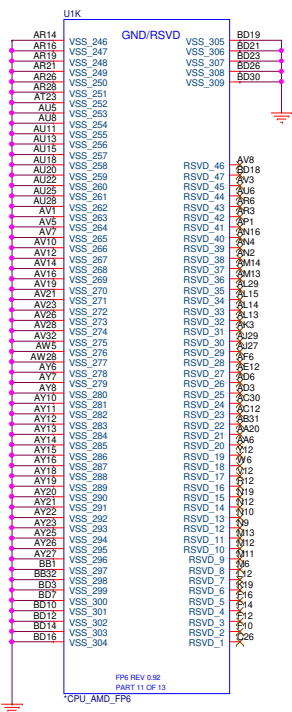
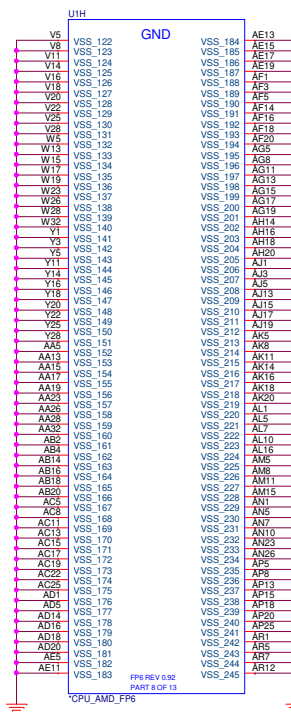
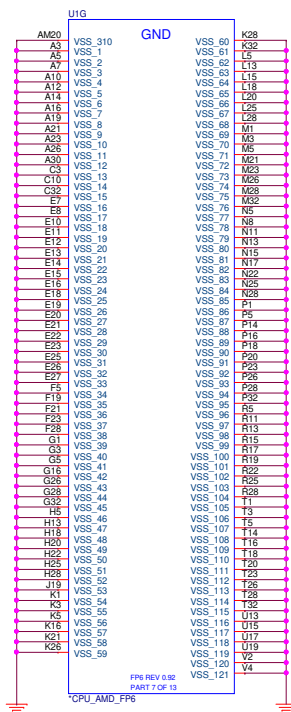


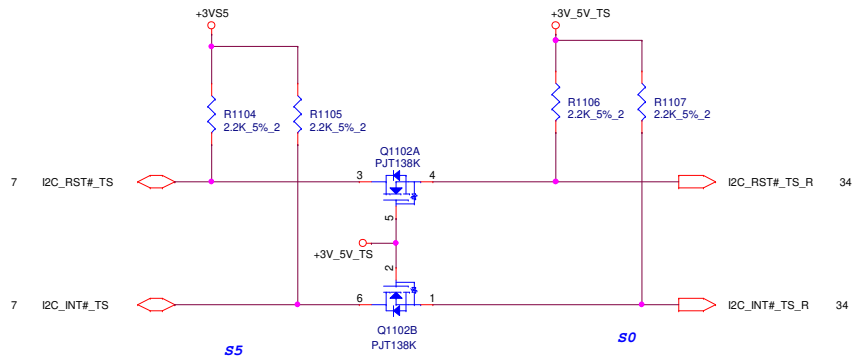
BOARD ID




PROJECT : G7HA
Quanta Computer Inc.









NA

	PROJECT : G7HA Quanta Computer Inc.		
	Size A	Document Number PCH(NA)	Rev 1A
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NA

	PROJECT : G7HA Quanta Computer Inc.		
	Size A	Document Number PCH(NA)	Rev 1A
	2 of 106		

NA




NB5

PROJECT : G7HA
Quanta Computer Inc.


Size A	Document Number PCH(NA)	Rev 1A
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NA

	PROJECT : G7HA Quanta Computer Inc.		
	Size A	Document Number PCH(NA)	Rev 1A
	of 106		

NA

	PROJECT : G7HA Quanta Computer Inc.		
	Size A	Document Number PCH(NA)	Rev 1A
	of 106		

NA



PROJECT : G7HA
Quanta Computer Inc.

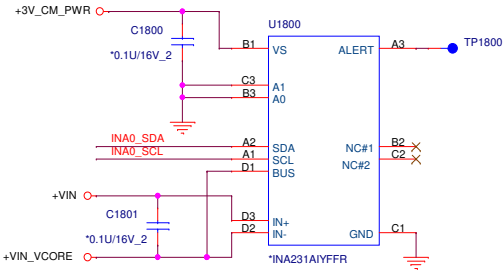
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Document Number
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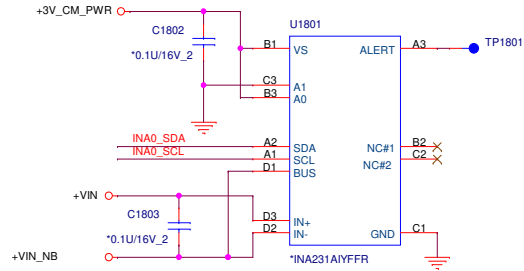
Rev
1A

2 of 106

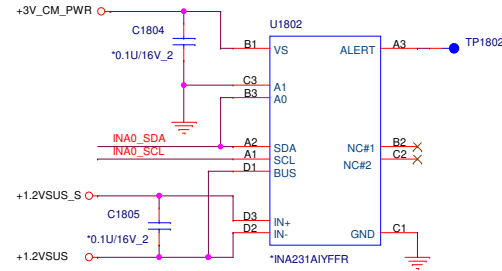
+VCCIN
Slave Addresses:1000000 Placed close to PR8900
INA0_SCL/SDA



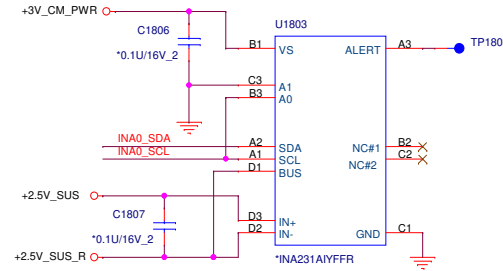
+VDDCR_SOC
Slave Addresses:1000001 Placed close to PR8907
INA0_SCL/SDA



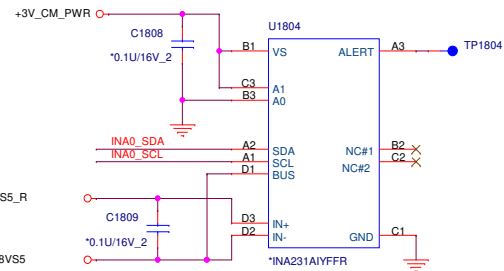
+1.2VSUS
Slave Addresses:1000010 Placed close to PR8713
INA0_SCL/SDA



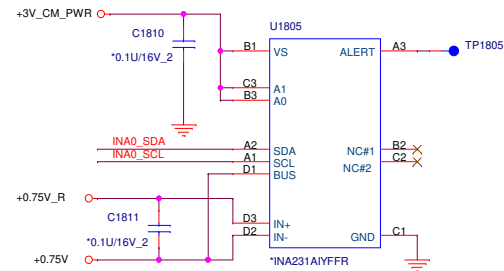
+2.5V_SUS
Slave Addresses:1000011 Placed close to PR8702
INA0_SCL/SDA



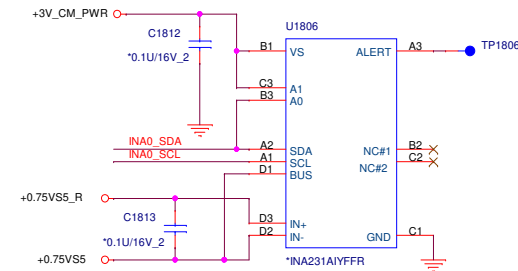
+1.8VS5
Slave Addresses:1000100 Placed close to PR9301
INA0_SCL/SDA



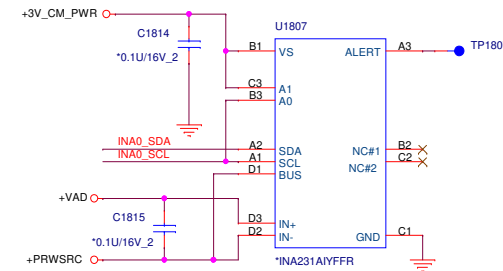
+0.75V
Slave Addresses:1000101 Placed close to PR9201
INA0_SCL/SDA



+0.75VS5
Slave Addresses:1000110 Placed close to PR9211
INA0_SCL/SDA



+VAD
Slave Addresses:1000111 Placed close to PR8200
INA0_SCL/SDA

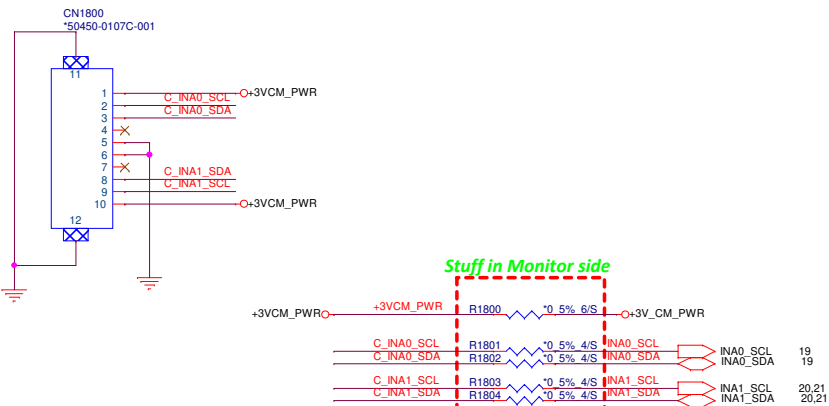


Need to confirm address

Need to confirm address

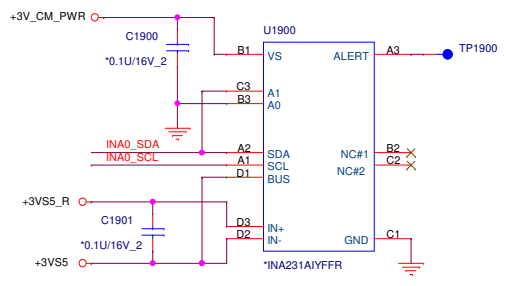
Table 7. INA231 Address Pins and Slave Addresses

A1	A0	SLAVE ADDRESS
GND	GND	1000000
GND	V _S	1000001
GND	SDA	1000010
GND	SCL	1000011
V _S	GND	1000100
V _S	V _S	1000101
V _S	SDA	1000110
V _S	SCL	1000111
SDA	GND	1001000
SDA	V _S	1001001
SDA	SDA	1001010
SDA	SCL	1001011
SCL	GND	1001100
SCL	V _S	1001101
SCL	SDA	1001110
SCL	SCL	1001111

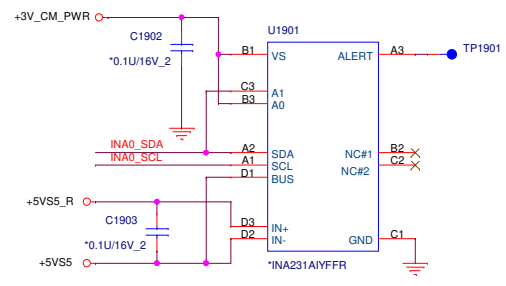


Stuff in Monitor side

+3VS5
Slave Addresses:1001000 Placed close to PR8604
INA0_SCL/SDA

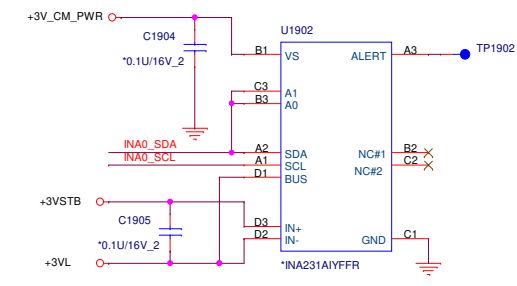


+5VS5
Slave Addresses:1001001 Placed close to PR8618
INA0_SCL/SDA

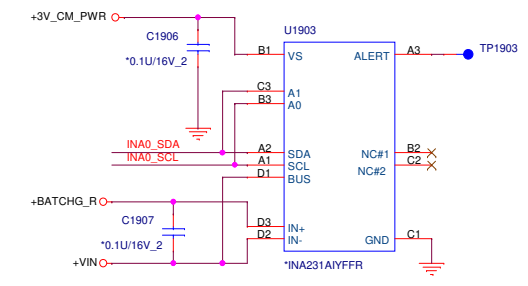


EC
Slave Addresses:1001010 Placed close to L4704
INA0_SCL/SDA

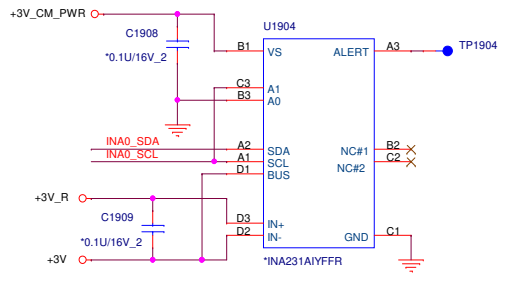
Need to confirm.



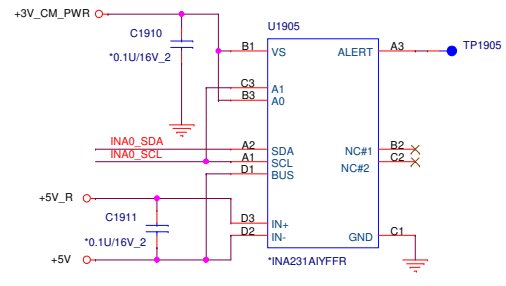
Battery (DC mode)
Slave Addresses:1001011 Placed close to PR8201
INA0_SCL/SDA



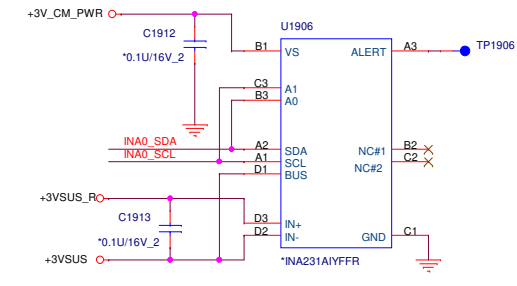
+3V
Slave Addresses:1001100 Placed close to PR9601
INA0_SCL/SDA



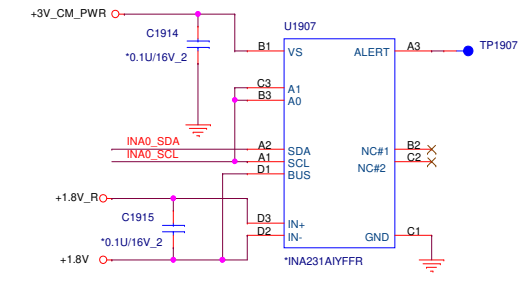
+5V
Slave Addresses:1001101 Placed close to PR9602
INA0_SCL/SDA



+3VSUS
Slave Addresses:1001110 Placed close to PR9600
INA0_SCL/SDA



+1.8V
Slave Addresses:1001111 Placed close to PR9603
INA0_SCL/SDA



+VGA_CORE
Slave Addresses:1000000 Placed close to PR9900
INA1_SCL/SDA



+1.35V_VGA
Slave Addresses:1000001 Placed close to PR10403
INA1_SCL/SDA

+VDDCI
Slave Addresses:1000010 Placed close to PR9907
INA1_SCL/SDA

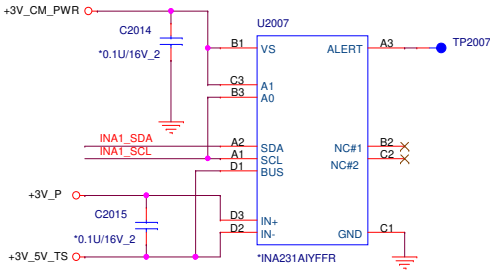
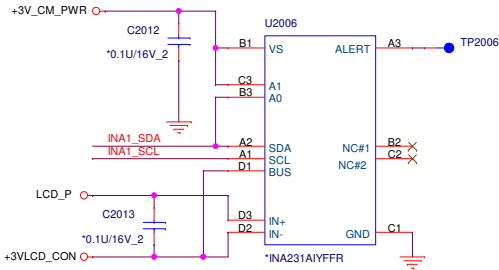
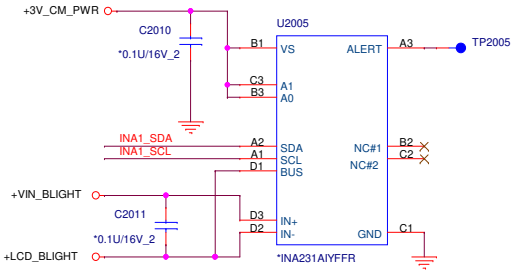
+1.8V_VGA
Slave Addresses:1000011 Placed close to PR10508
INA1_SCL/SDA

+3V_VGA
Slave Addresses:1000100 Placed close to PR10509
INA1_SCL/SDA

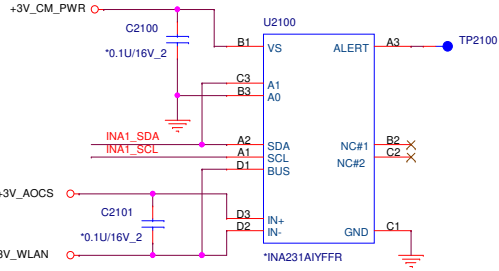
LCD Backlight
Slave Addresses:1000101 Placed close to L3405
INA1_SCL/SDA

eDP
Slave Addresses:1000110 Placed close to L3406
INA1_SCL/SDA

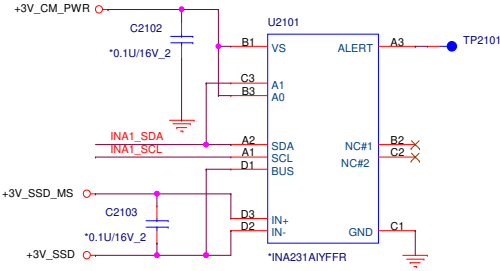
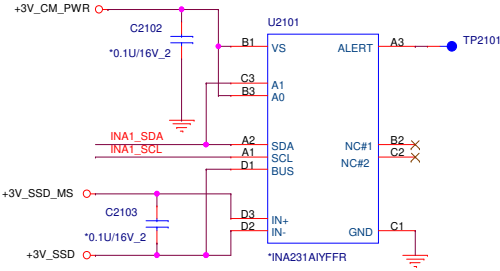
Touch Screen
Slave Addresses:1000111 Placed close to R3417
INA1_SCL/SDA



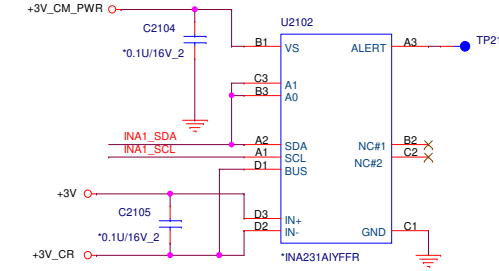
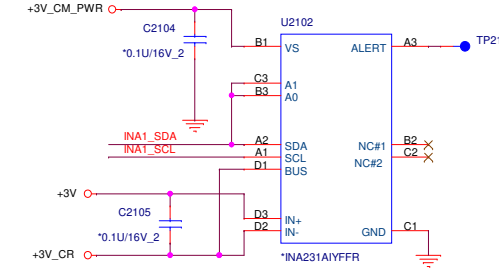
WLAN
Slave Addresses:1001000 Placed close to L4100
INA1_SCL/SDA



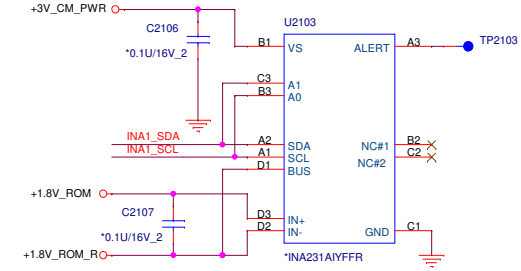
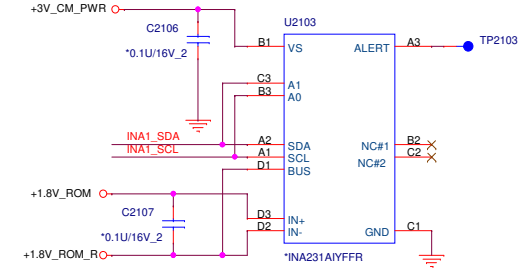
SSD
Slave Addresses:1001001 Placed close to R4300
INA1_SCL/SDA



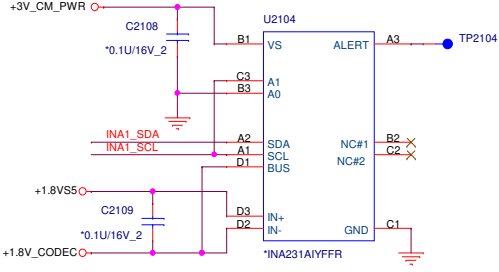
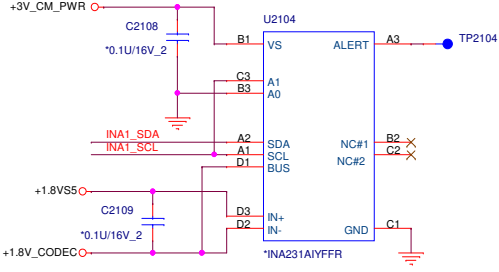
Card Reader
Slave Addresses:1001010 Placed close to R5510
INA1_SCL/SDA



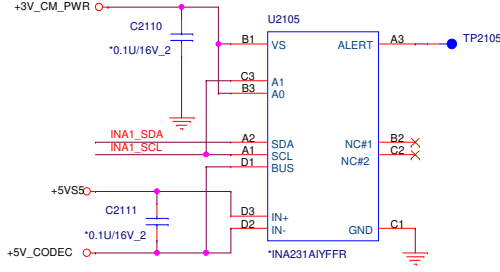
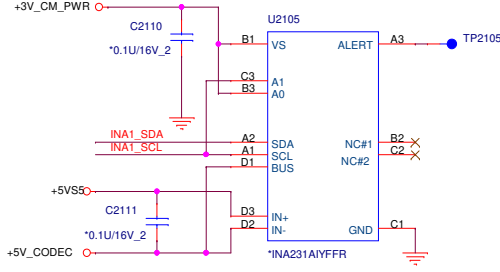
SPI ROM
Slave Addresses:1001011 Placed close to R5612
INA1_SCL/SDA



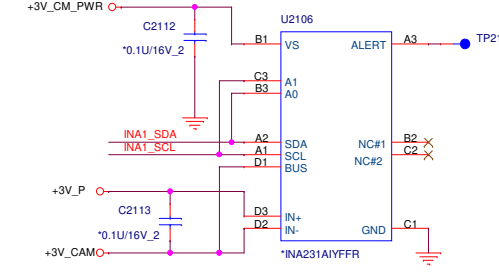
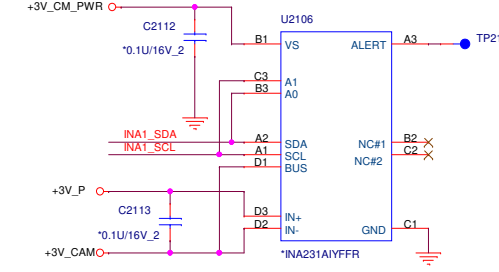
Codec +1.8V
Slave Addresses:1001100 Placed close to R3618
INA1_SCL/SDA



Codec +5V
Slave Addresses:1001101 Placed close to R3617
INA1_SCL/SDA



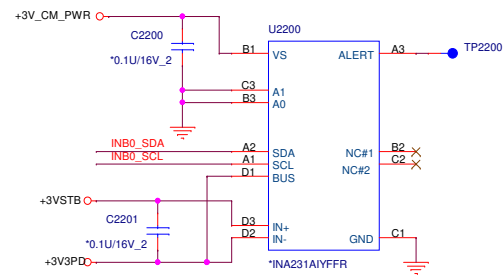
CAMERA
Slave Addresses:1001110 Placed close to R3427
INA1_SCL/SDA



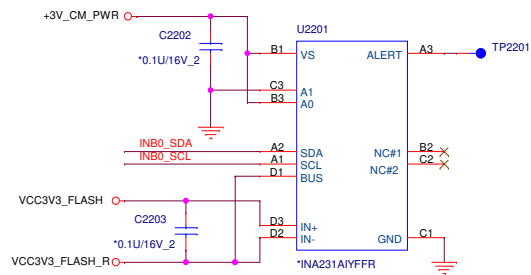
+VDDCI
Slave Addresses:1001111 Placed close to PJP7100
INA1_SCL/SDA



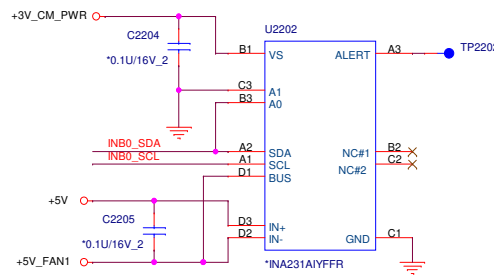
PD
Slave Addresses:1000000 Placed close to R6101
INB0_SCL/SDA



PD_ROM
Slave Addresses:1000001 Placed close to R6117
INB0_SCL/SDA



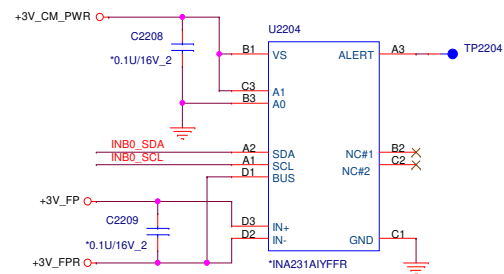
FAN1
Slave Addresses:1000010 Placed close to R5405
INB0_SCL/SDA



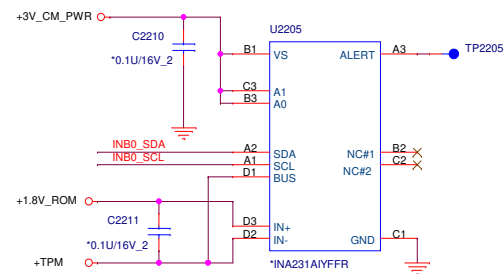
FAN2
Slave Addresses:1000011 Placed close to R5403
INB0_SCL/SDA



Finger Print
Slave Addresses:1000100 Placed close to R5223
INB0_SCL/SDA



TPM
Slave Addresses:1000101 Placed close to RT5100
INB0_SCL/SDA



HDMI
Slave Addresses:1000110 Placed close to R3505
INB0_SCL/SDA



HDMI Re-driver
Slave Addresses:1000111 Placed close to R3500
INB0_SCL/SDA

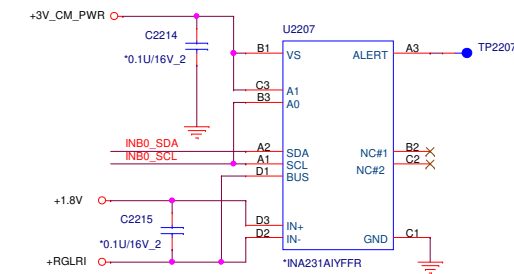
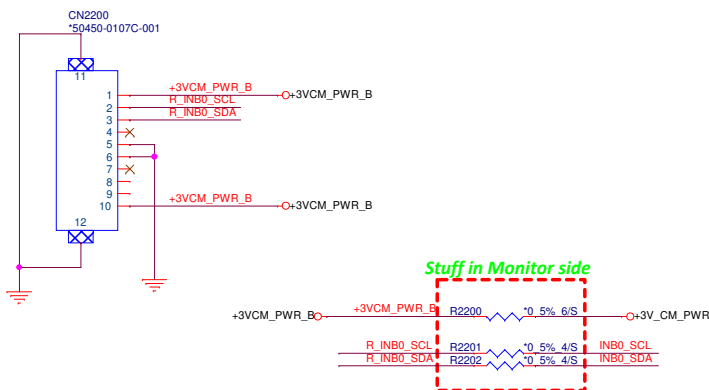



Table 7. INA231 Address Pins and Slave Addresses

A1	A0	SLAVE ADDRESS
GND	GND	1000000
GND	V _S	1000001
GND	SDA	1000010
GND	SCL	1000011
V _S	GND	1000100
V _S	V _S	1000101
V _S	SDA	1000110
V _S	SCL	1000111
SDA	GND	1001000
SDA	V _S	1001001
SDA	SDA	1001010
SDA	SCL	1001011
SCL	GND	1001100
SCL	V _S	1001101
SCL	SDA	1001110
SCL	SCL	1001111




PCH



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Quanta Computer Inc.

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	PCH	1A
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PCH



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Quanta Computer Inc.

Size	Document Number	Rev
	PCH	1A
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PCH



PROJECT : G7HA
Quanta Computer Inc.

Size	Document Number	Rev
	25	1A
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PCH



PROJECT : G7HA

Quanta Computer Inc.

Size	Document Number	Rev
NB5	PCH	1A
Date: Friday, July 10, 2020		Sheet6 of 106

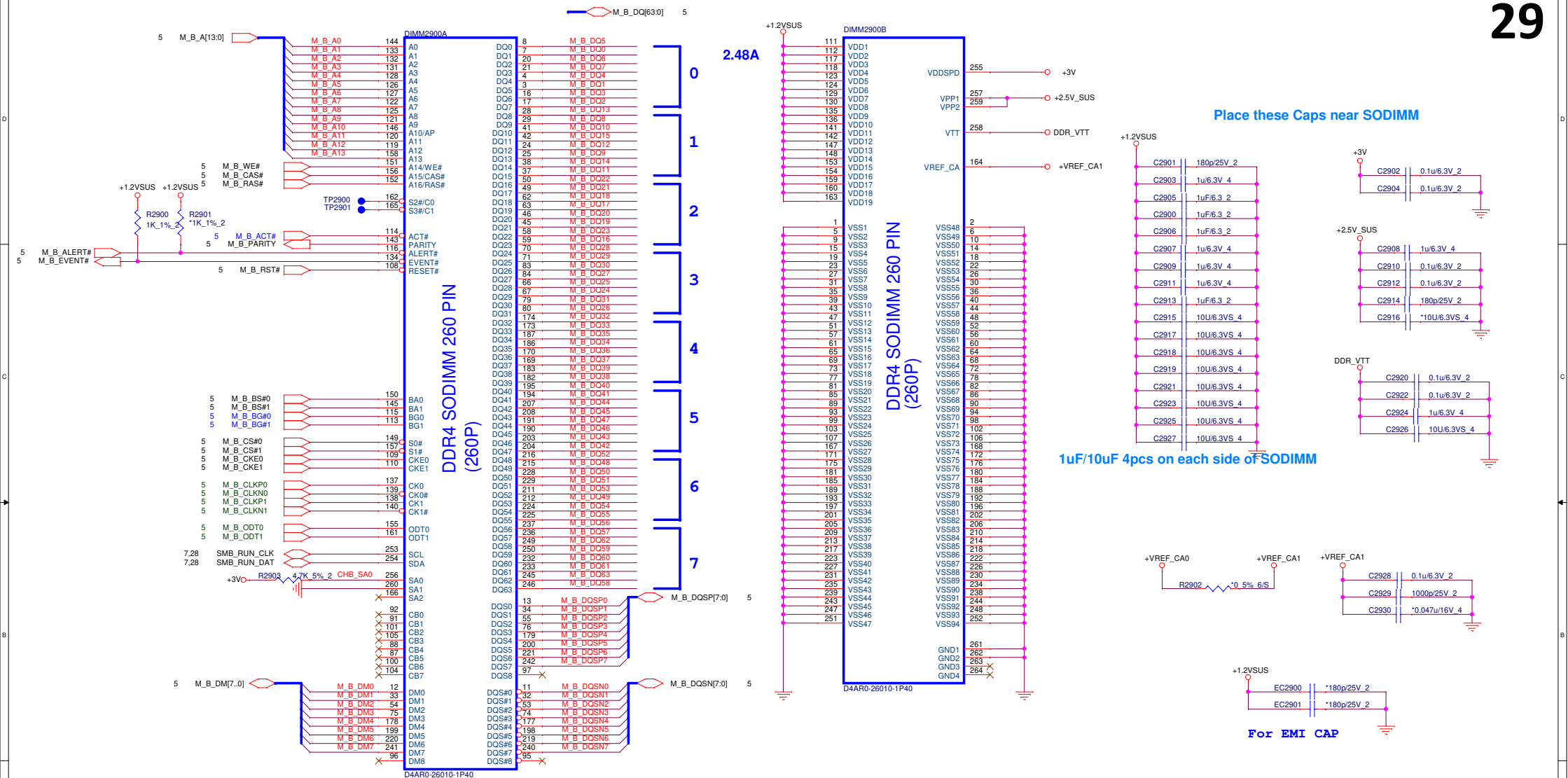
Functional Strap Definitions

From page47



0110:back to CRB setting 1K


For EMI CAP



MEMORY

MEMORY

PCH

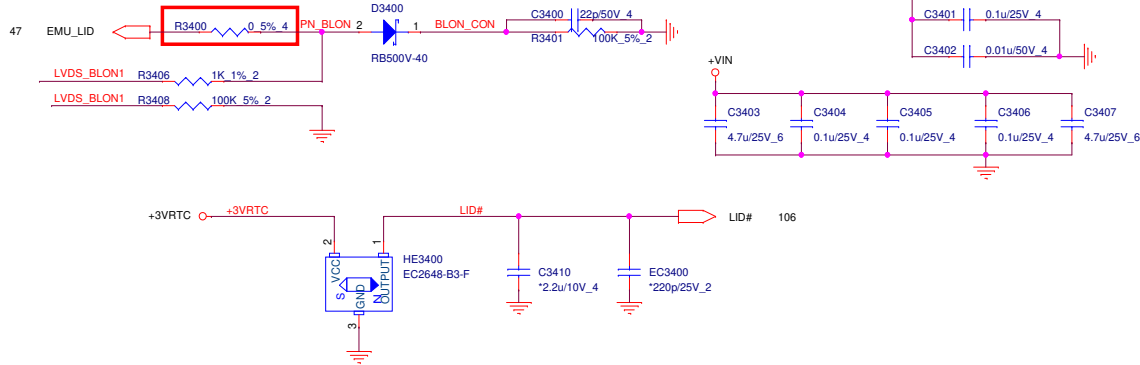


PROJECT : G7HA
Quanta Computer Inc.

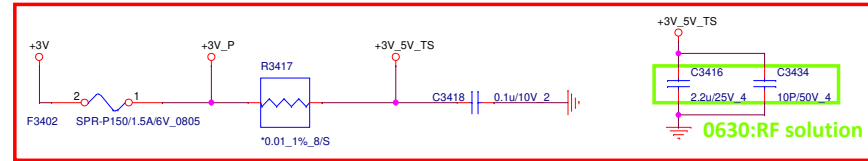
Size	Document Number	Rev
	I2C Path	1A
Date: Friday, July 10, 2020		Sheet 3 of 106

LID Switch

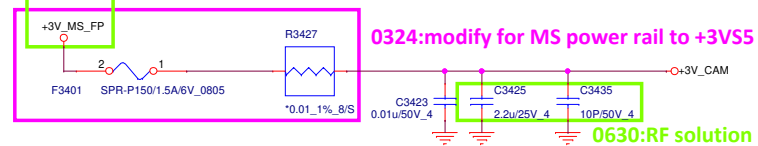
R3400 can't change to shortpad



Touch screen



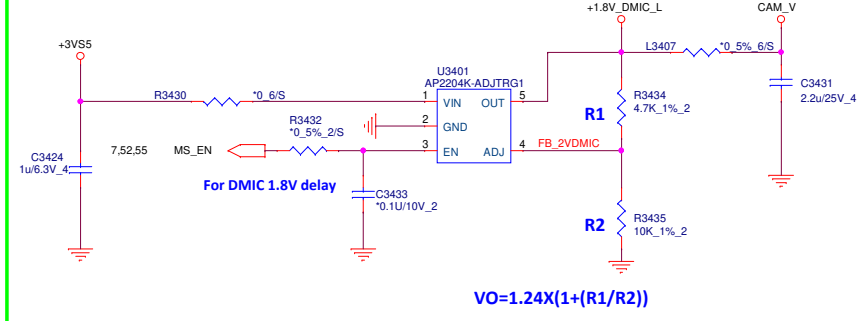
0630:modify for MS power rail to +3V_MS_FP



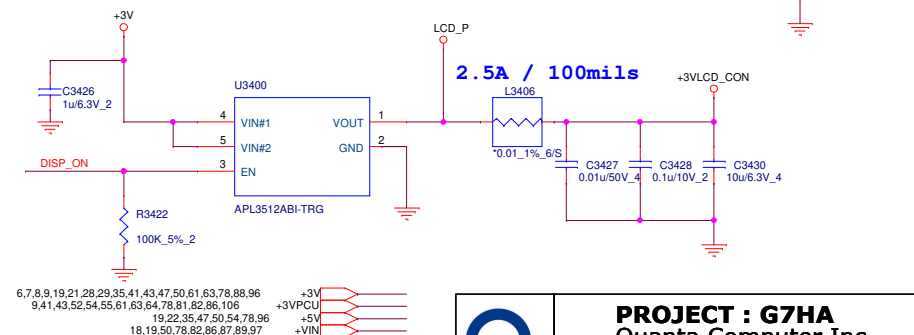
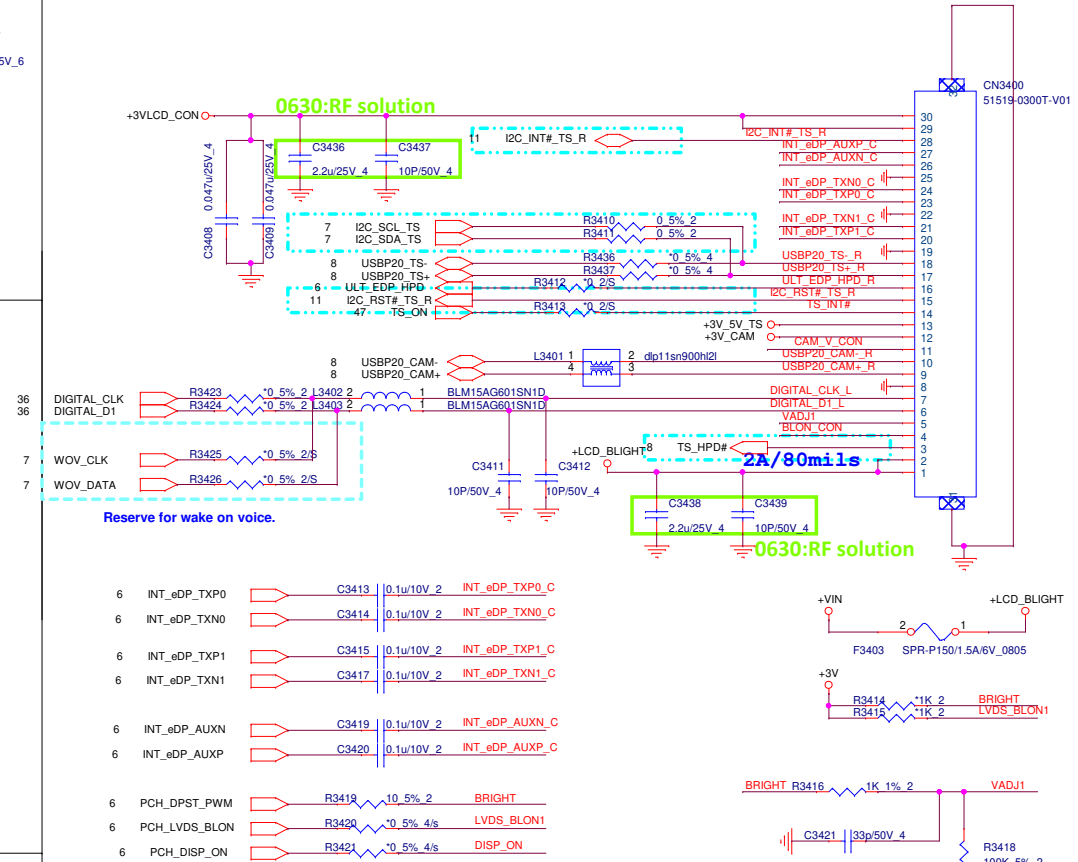
Camera power

0121 : modify for MS

DMIC PWR



eDP Conn.



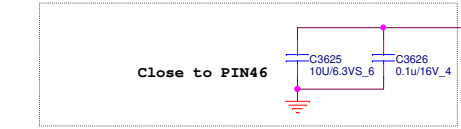
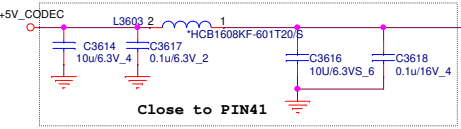
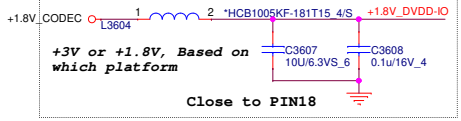
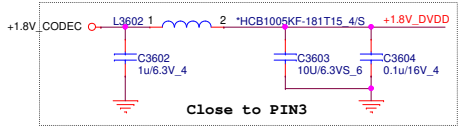
PROJECT : G7HA
Quanta Computer Inc.

Size	Document Number	Rev
Custom	eDP/IR CAM/LID/CAM/MIC/TS	1A
Date: Friday, July 10, 2020	Sheet 34 of 106	

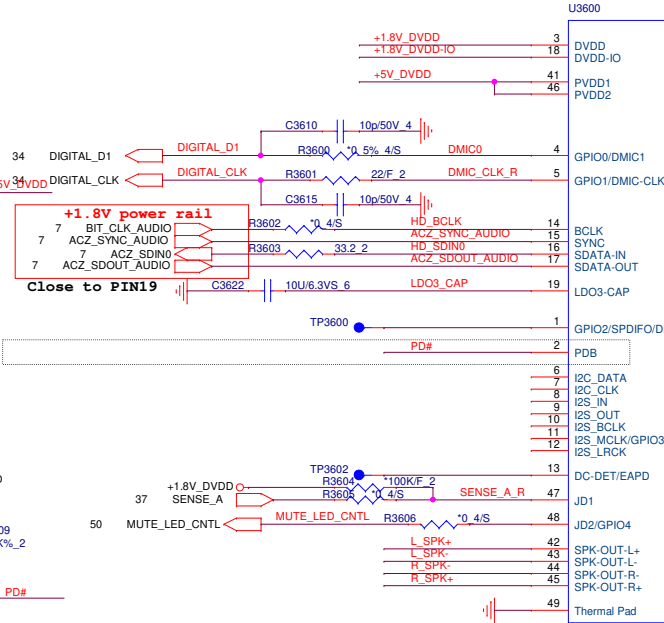
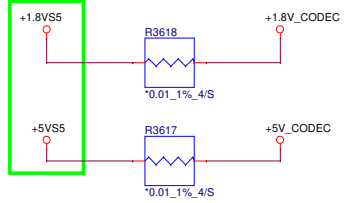
ALC3314-CG

36

+3V or +1.8V, Based on which platform

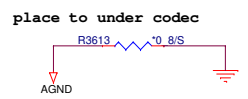
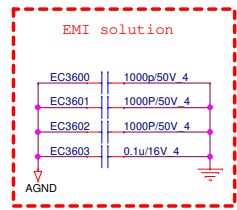
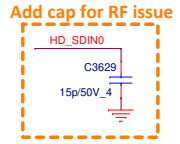


0121 : modify for MS

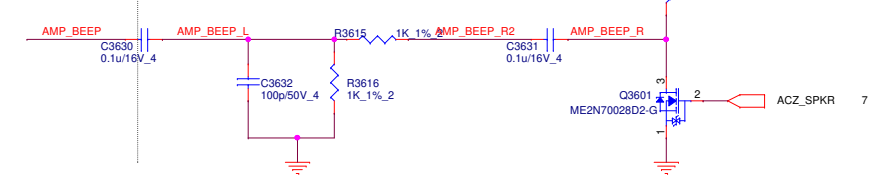


Digital
Analog

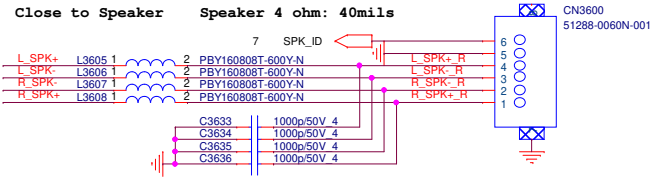
Speaker 4 ohm: 40mils



Analog
Digital

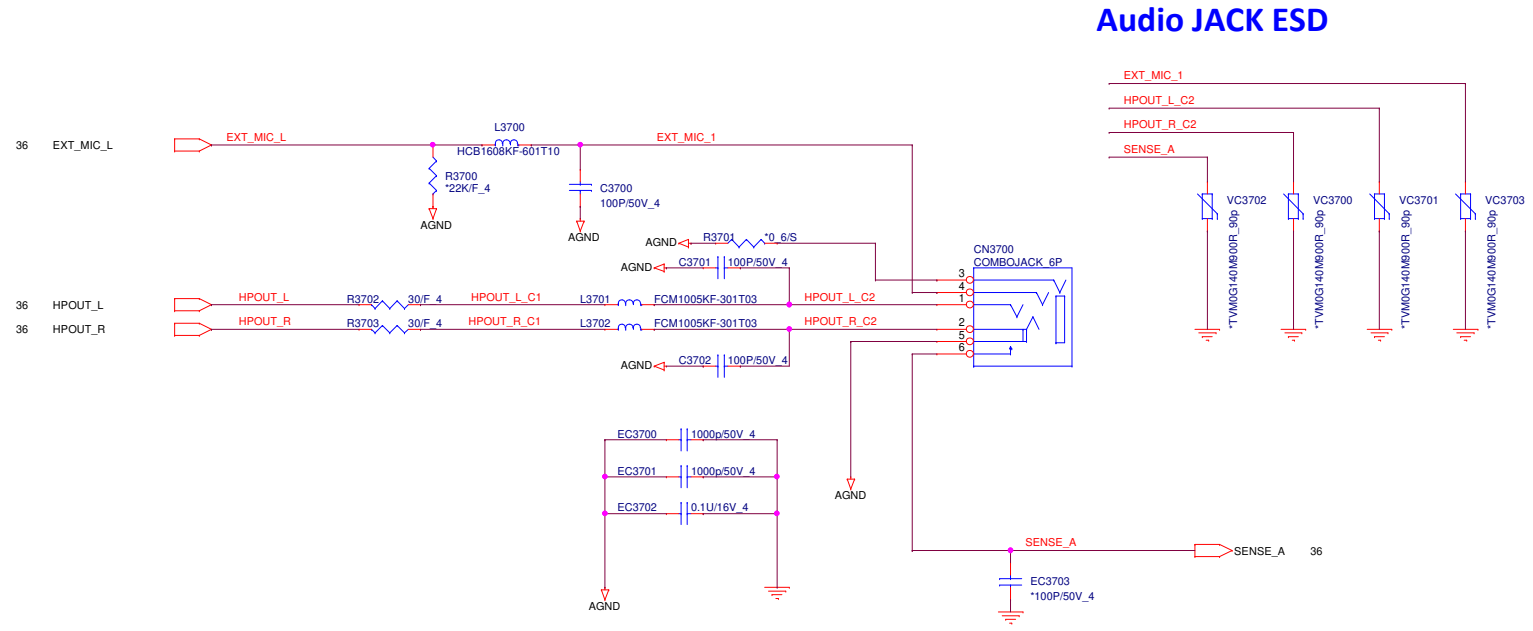


SPK CONN




SPK_ID for Smart amp feature

Audio Combo Jack




AUDIO



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Size Custom	Document Number Audio	Rev 1A
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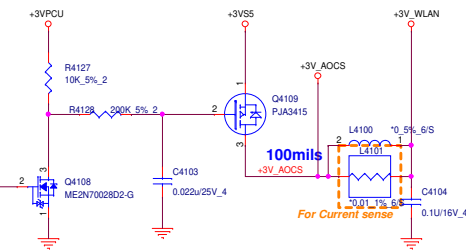
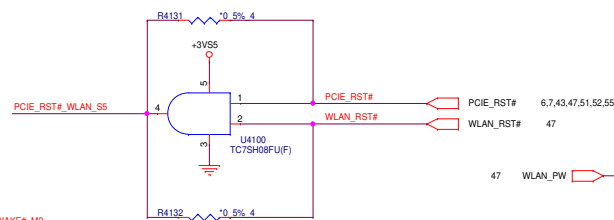
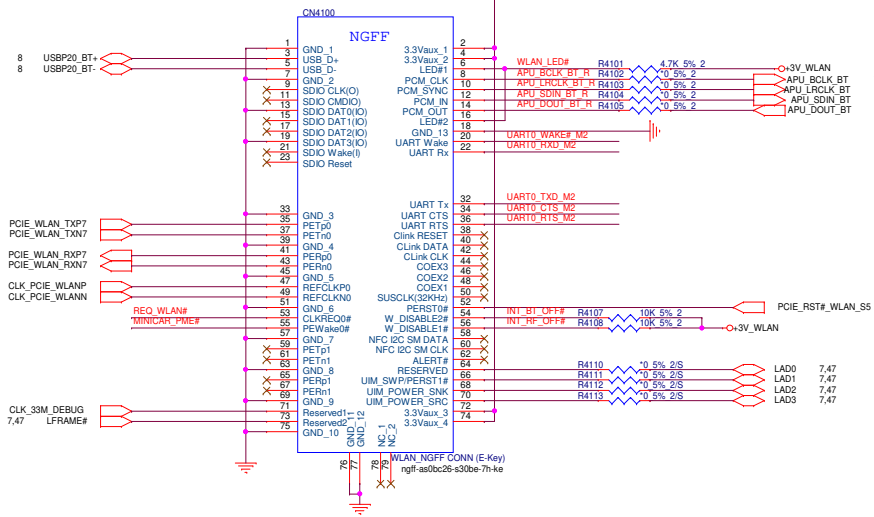
AUDIO



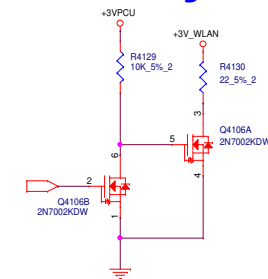
PROJECT : G7HA
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+3V_WLA



+3VPCU



PCH




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Size Custom	Document Number WWAN	Rev 1A
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PCH

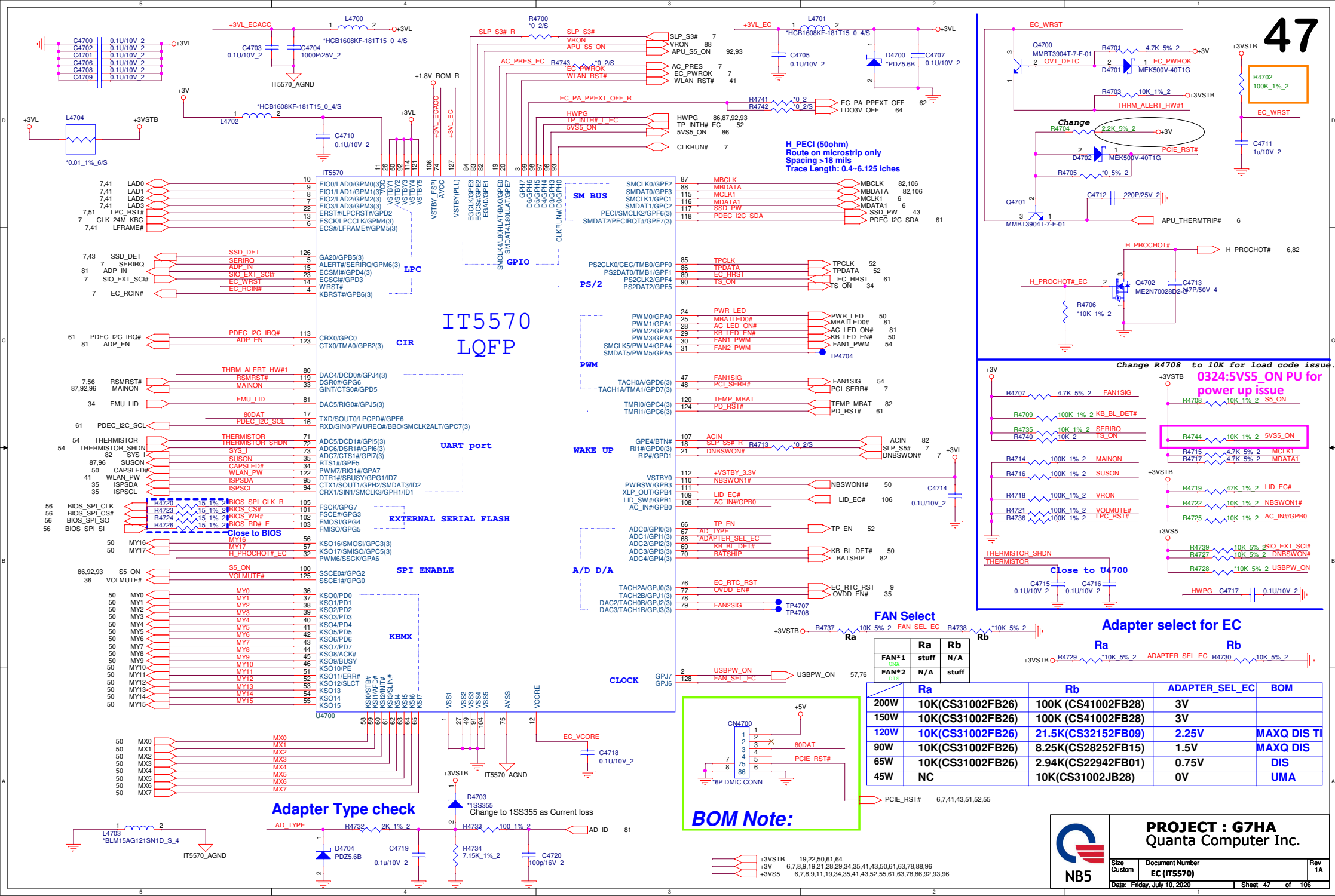


PROJECT : G7HA

Quanta Computer Inc.

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CAMERA



H. PEGI (50ohm)
Route on microstrip only
Spacing >18 mils
Trace Length: 0.4-6.125 inches

IT5570
LQFP

UART port

EXTERNAL SERIAL FLASH

SPI ENABLE

KBMX

Adapter Type check

BOM Note:

FAN Select

Adapter select for EC

FAN#1	Ra	Rb
stuff	stuff	N/A
FAN#2	Ra	Rb
DIS	N/A	stuff

	Ra	Rb	ADAPTER_SEL_EC	BOM
200W	10K(CS31002FB26)	100K (CS41002FB28)	3V	
150W	10K(CS31002FB26)	100K (CS41002FB28)	3V	
120W	10K(CS31002FB26)	21.5K(CS32152FB09)	2.25V	MAXQ DIS TI
90W	10K(CS31002FB26)	8.25K(CS28252FB15)	1.5V	MAXQ DIS
65W	10K(CS31002FB26)	2.94K(CS22942FB01)	0.75V	DIS
45W	NC	10K(CS31002JB28)	0V	UMA



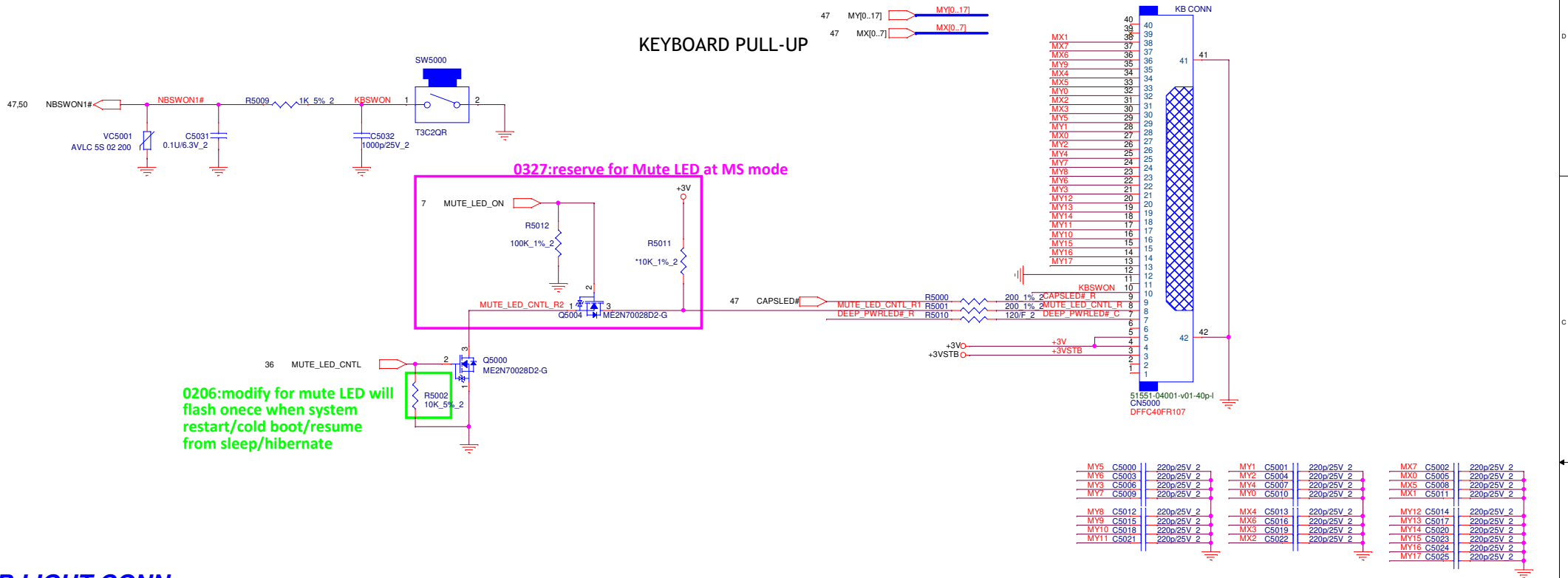
PROJECT : G7HA
Quanta Computer Inc.

PCH

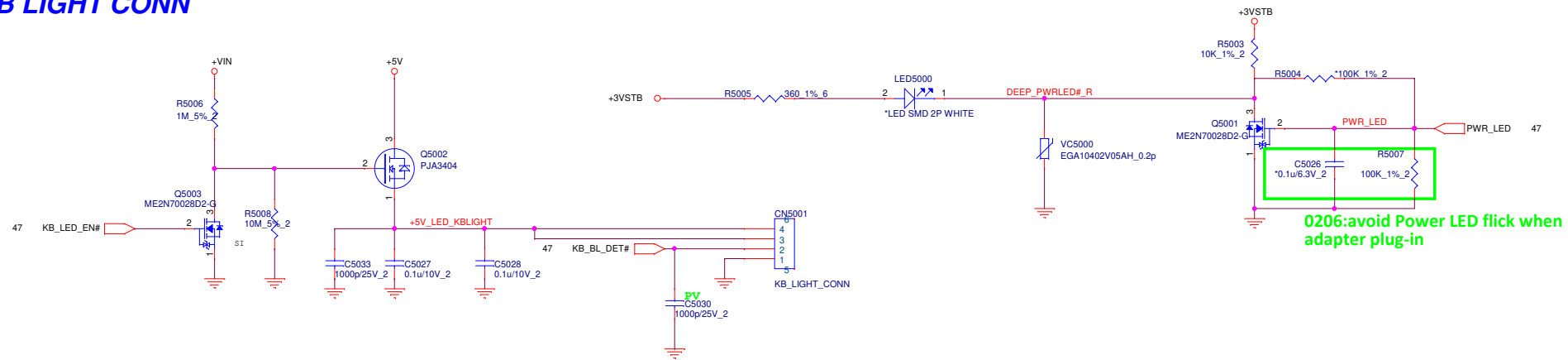
PCH

Stuff PW button for test in factory.

KEYBOARD Con.



KB LIGHT CONN



6,7,8,9,11,19,34,35,41,43,47,52,55,61,63,78,86,92,93,96
6,7,8,9,19,21,28,29,34,35,41,43,47,61,63,78,88,96
19,21,36,57,61,76,78,86,87,88,96
47,50
+3VS5
+3V
+5VS5
+TYPEC_VBUS
NBSWON1#



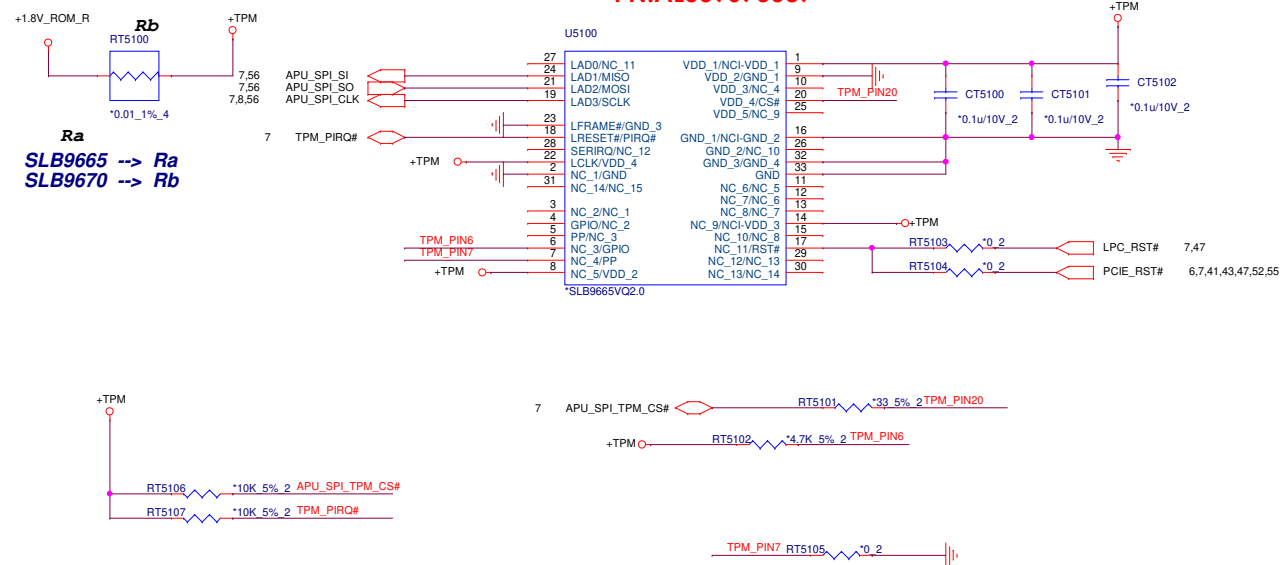
PROJECT : G7HA
Quanta Computer Inc.

Size Custom	Document Number KB/KB LIGHT	Rev 1A
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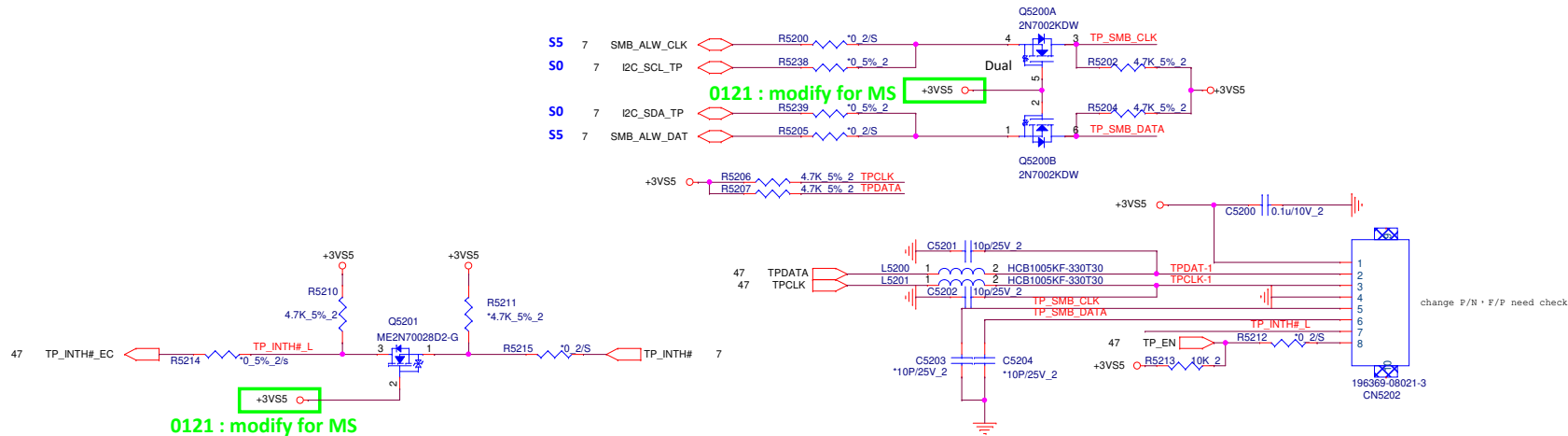
TPM

TPM (2.0)

FW 7.83 PN:AL009670037



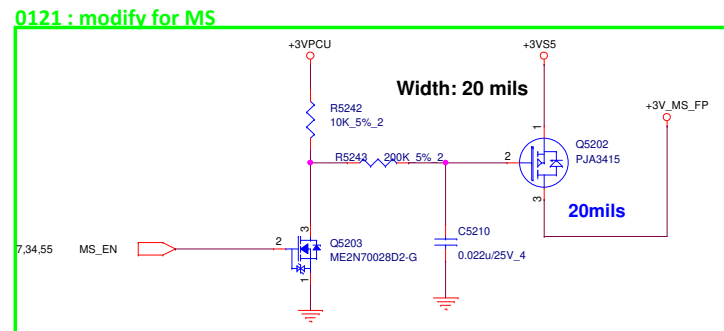
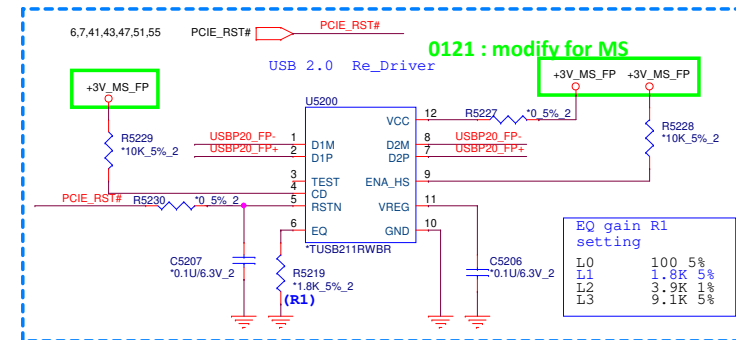
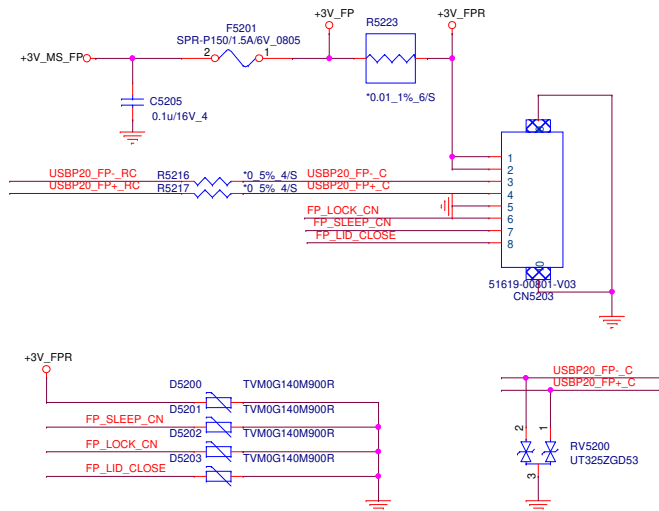
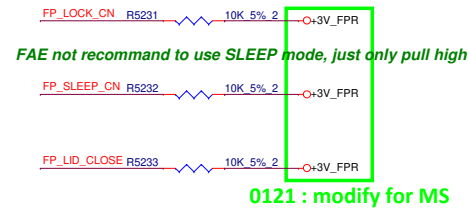
For 9670 stuff



Finger print

Lock_N for lock ROM data, pull high is no lock and PCH pull low to lock

FAE not recommend to use SLEEP mode, just only pull high

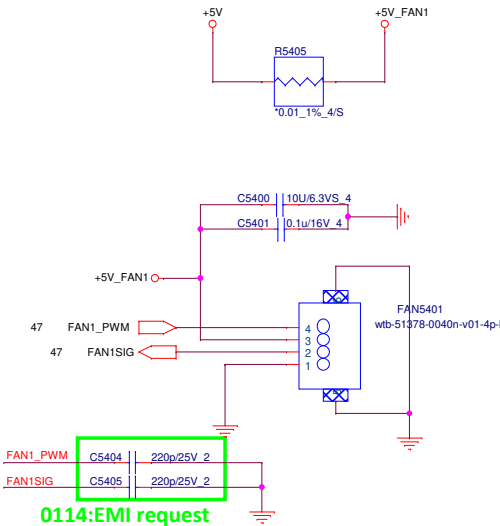


SENSOR

FAN/Thermal

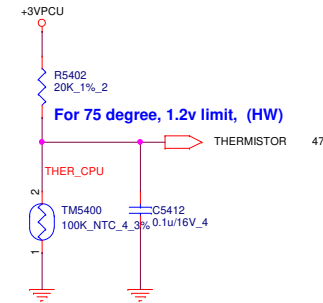
FAN1

FAN2

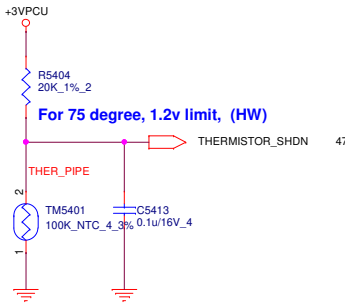


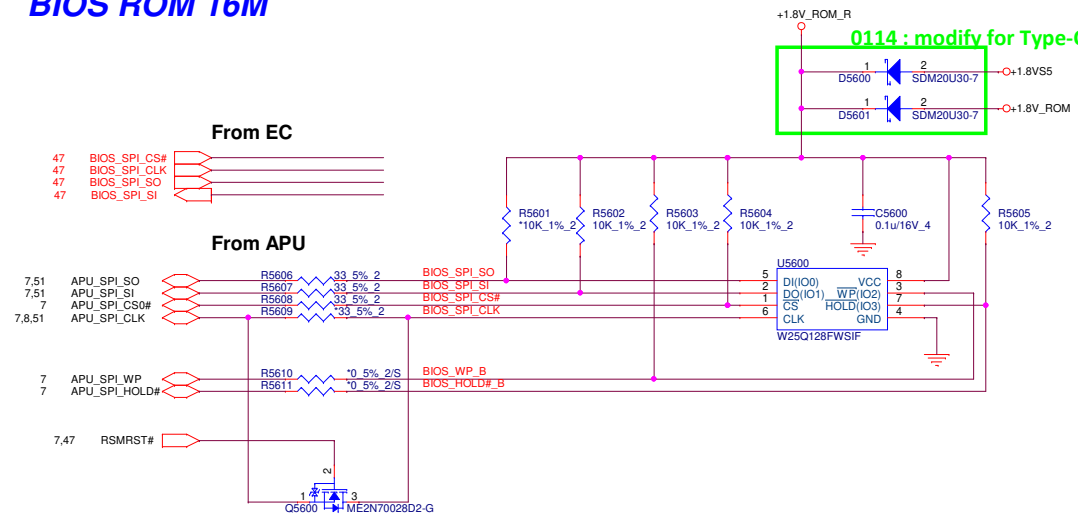
IO Thrm Protect

For PIPE USE



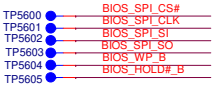
For CPU USE





0114 : modify for Type-C adapter

TP (TP2675) need place to the same layer



FLASH ROM

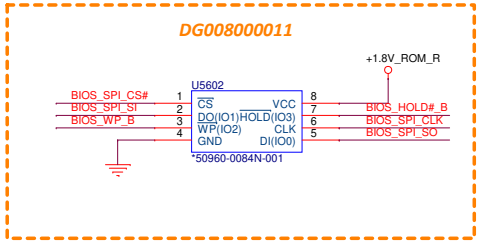
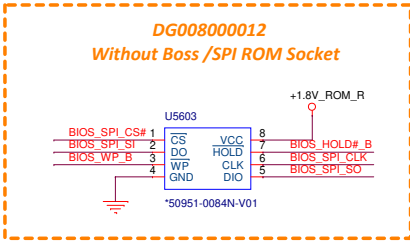
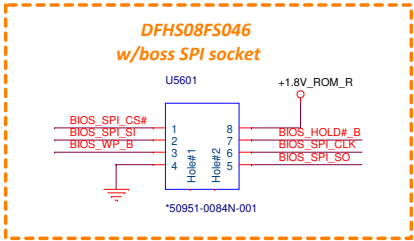
PV stuff SOP8 BIOS ROM only

Vender	Size	P/N (1.8V)
MAX	16M	AKE5DZN0Z00 MX25U12873FM2I-10G
GGD	16M	AKE5DZN0Q02 GD25LQ128DSIGR
WND	16M	AKE5DZN0N01 W25Q128FWSIF
Socket	DG008000011	

DB & SI stuff WSON8 SOCKETwith BIOS ROM

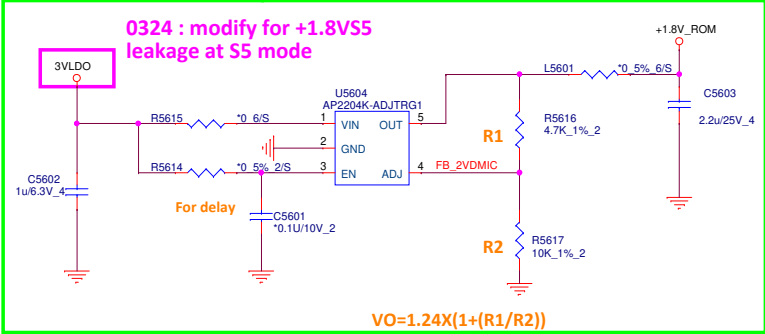
Vender	Size	P/N (1.8V)
MAX	16M	AKE5DFN0Z05 MX25U12835FZNI-10G
GGD	16M	AKE5DZKNQ01 GD25LQ128DWIGR
WND	16M	AKE5DFNKN01 W25Q128FWPIF
Socket	DG008000012	

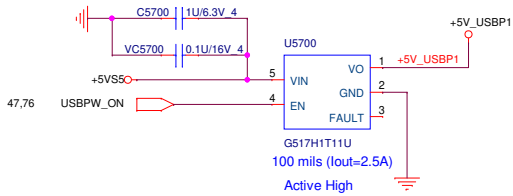
U5600,U5601,U5602,U5603 co-lay



0110 : Add for Dead battery

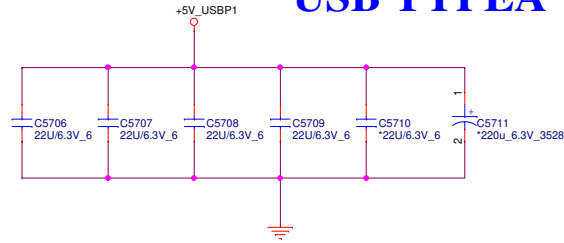
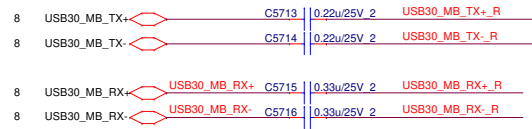
0324 : modify for +1.8VSS leakage at S5 mode



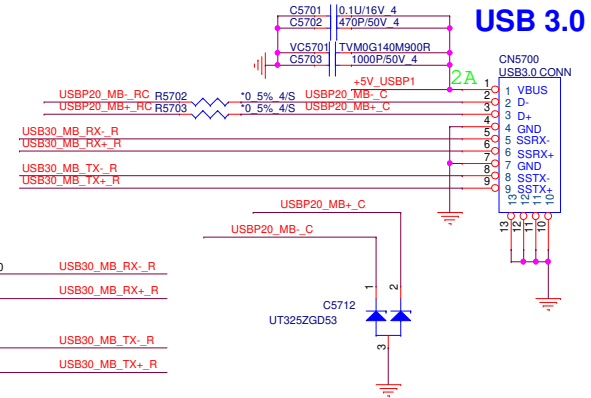
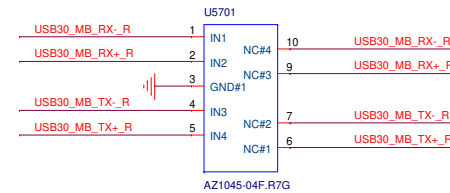
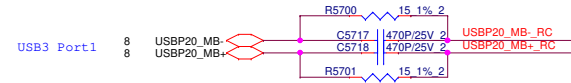


Active High

USB3.0 PORT0



USB TYPEA



USB 3.0

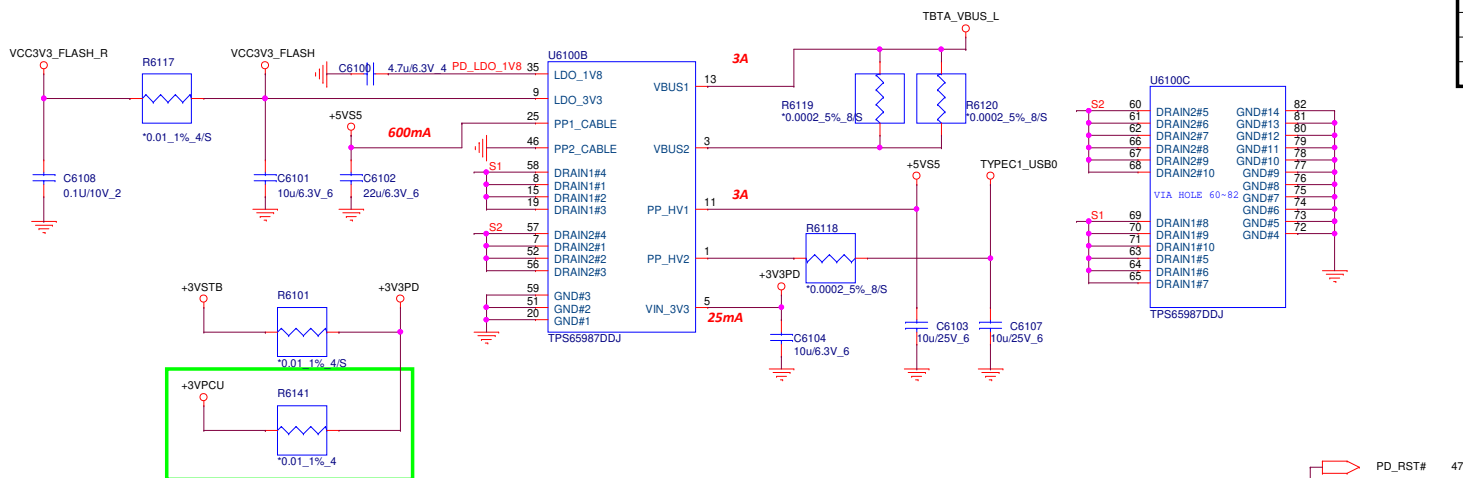
TBT

TBT

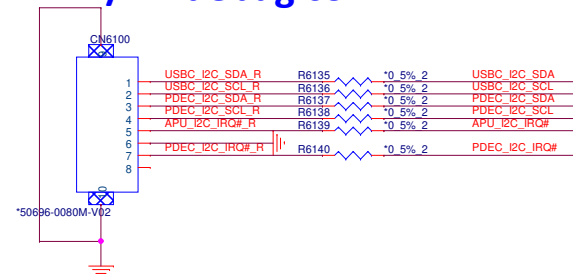
TBT

PD TPS65987DDJRSR

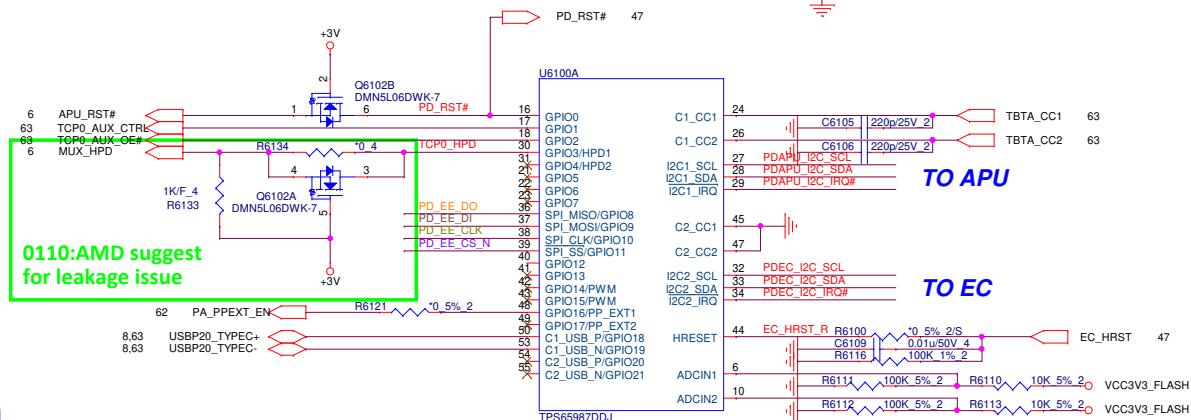
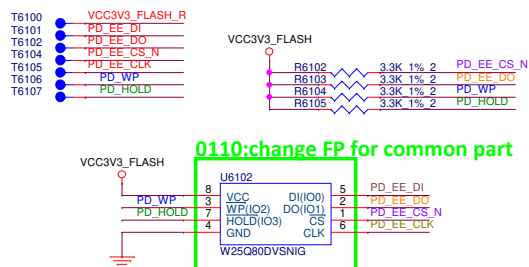
Qunata PN	Part Description
AL180901001	IC OTHER(56P) SN1809019RSHR(VQFN)
AL065987004	IC OTHER(56P)TPS65987DDJRSR(VQFN)



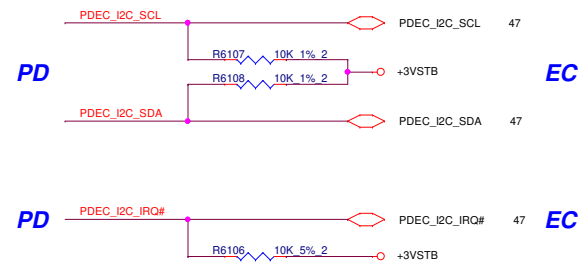
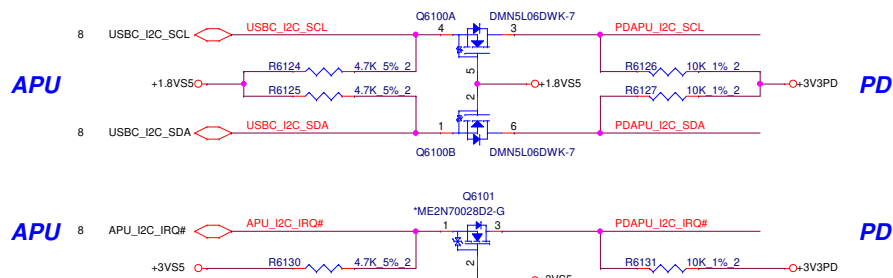
TBT/PD debug conn.



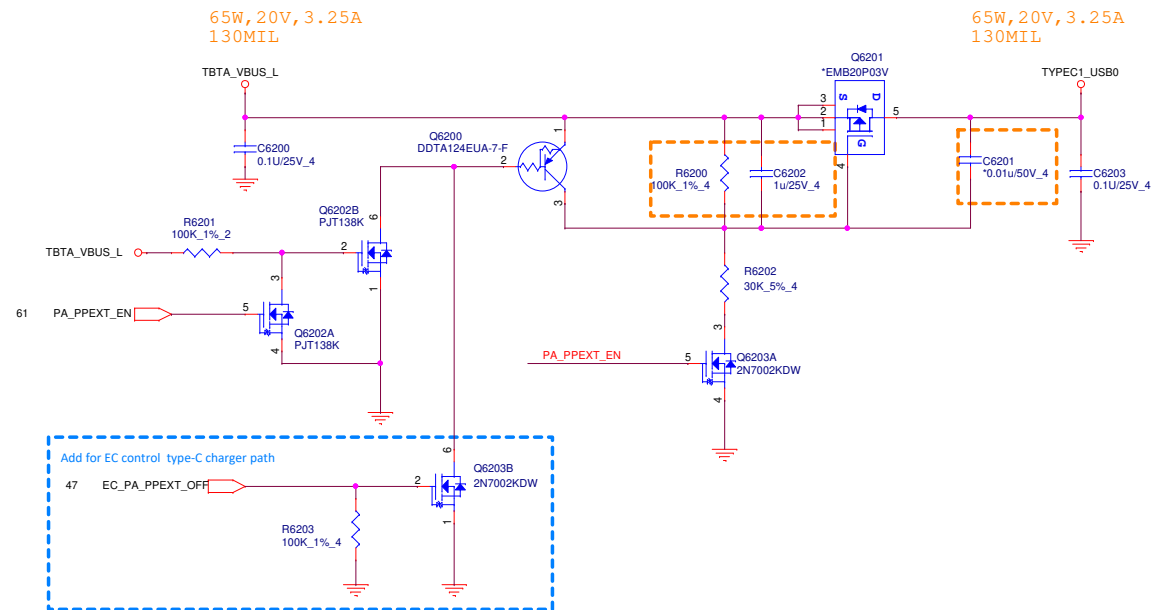
PD SPI ROM



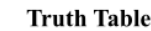
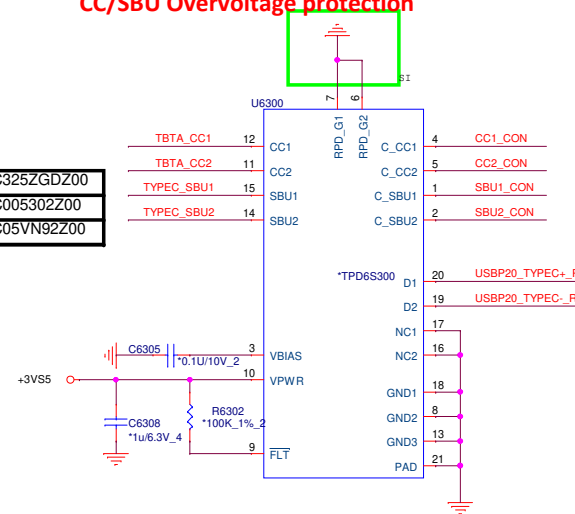
Vender	Quanta PN	Description	Size
Windbond	AKE3GGN0N02	IC FLASH (8P)W25Q80DVSNI6 (SOIC)	8Mb
Macronix	AKE3GFP0Z01	IC FLASH(8P) MX25L8006EM1I-12G(SOP)	8Mb



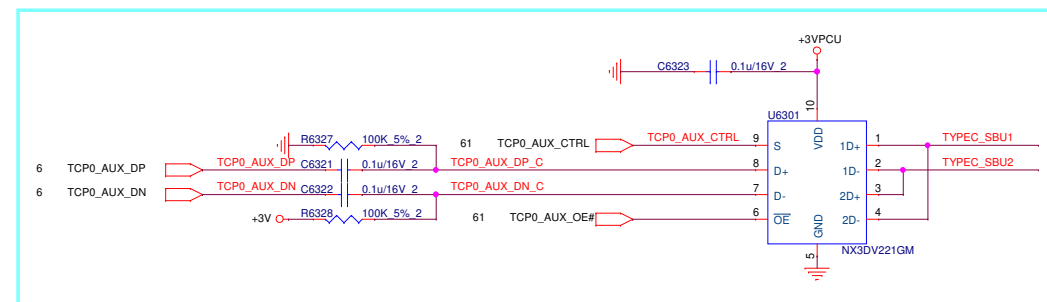
USB TYPEC PD



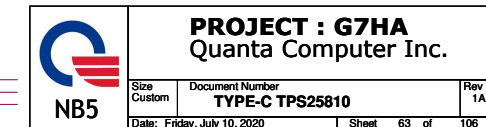
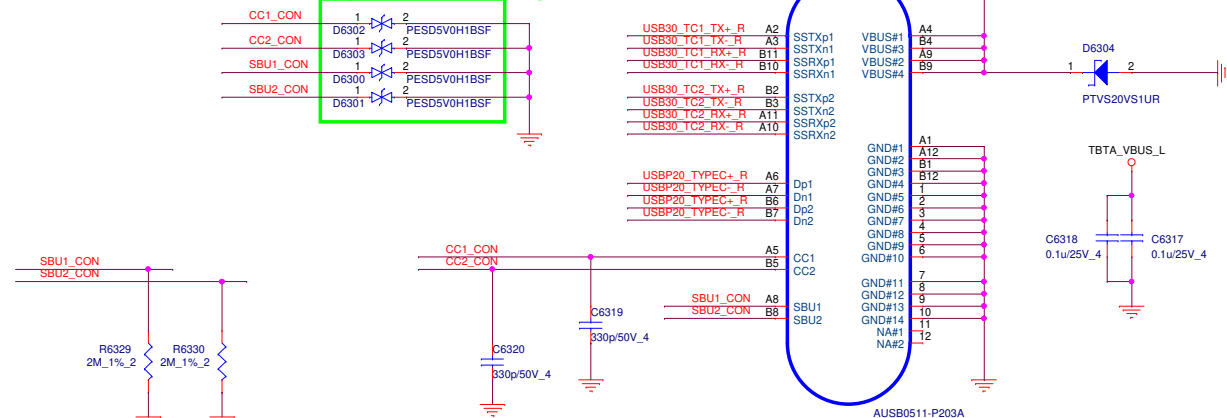
CC/SBU Overvoltage protection

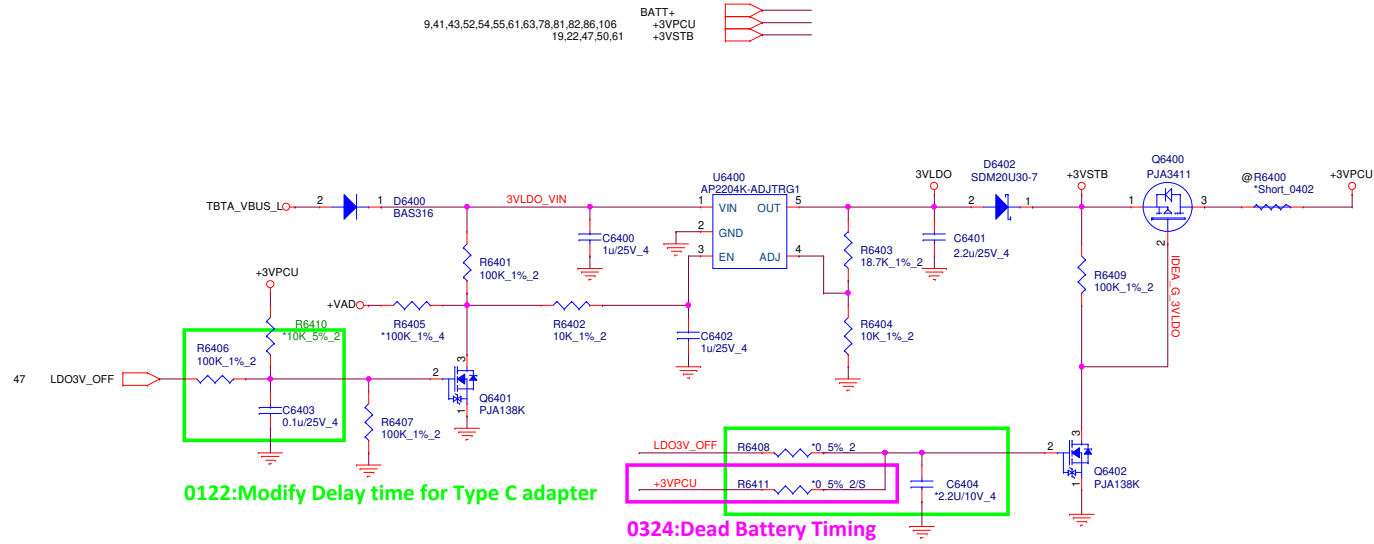


S	$\overline{\text{OE}}$	Function
X	H	Disconnect
L	L	D = 1D
H	L	D = 2D



0110:change FP for SMT highlight





PCH

Daughter Board

+3VPCU

+5VS5

+1.8V_DEEP_SUS

+3V

9,41,43,52,54,55,61,63,64,78,81,82,86,106

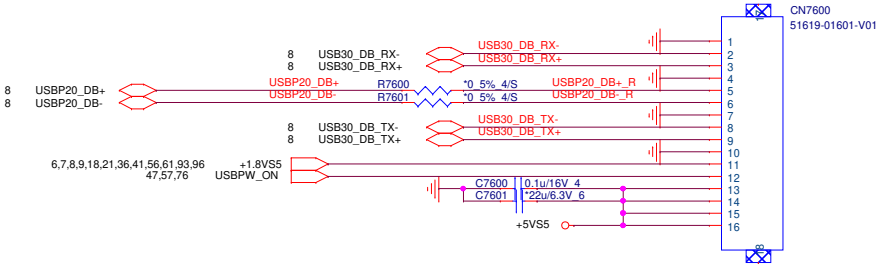
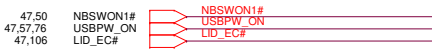
19,21,36,57,61,78,86,87,88,96

6,7,8,9,19,21,28,29,34,35,41,43,47,50,61,63,78,88,96

6,7,8,9,19,21,28,29,34,35,41,43,47,50,61,63,78,88,96

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USB Board



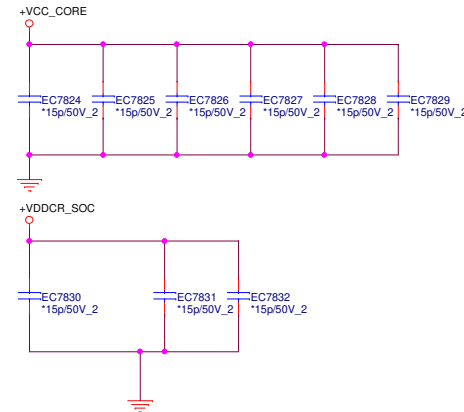
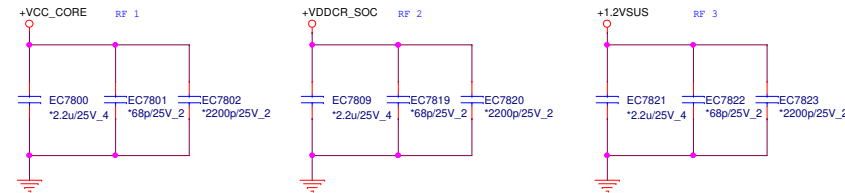
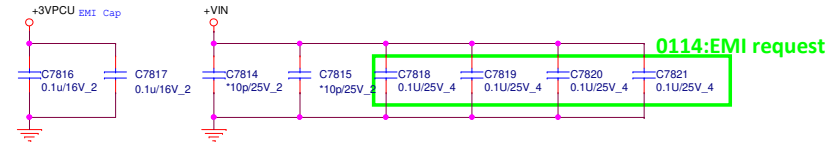
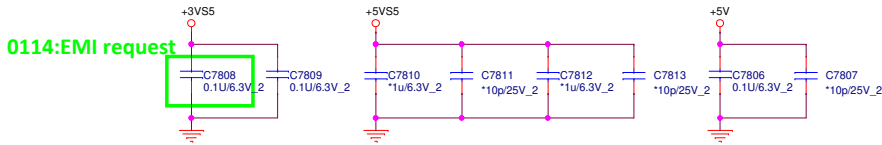
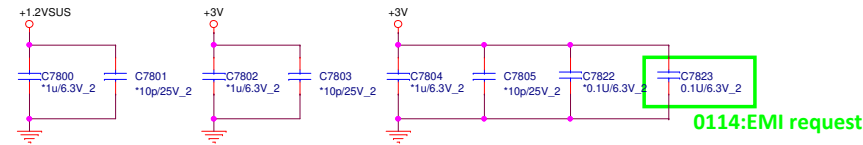
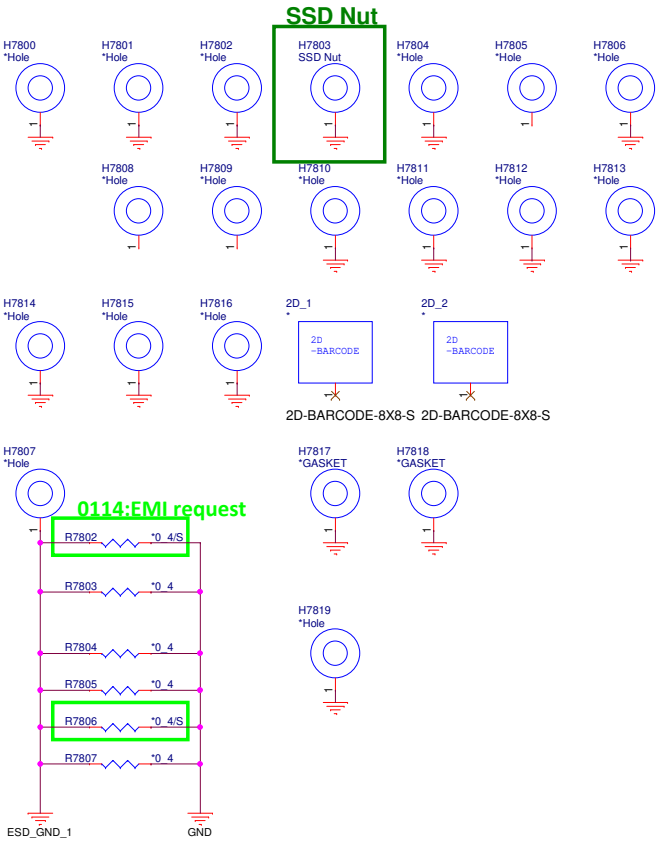
51 level BOM option 電阻

RA stuff TOP

R7800 10K_5%_2

RB stuff TOP

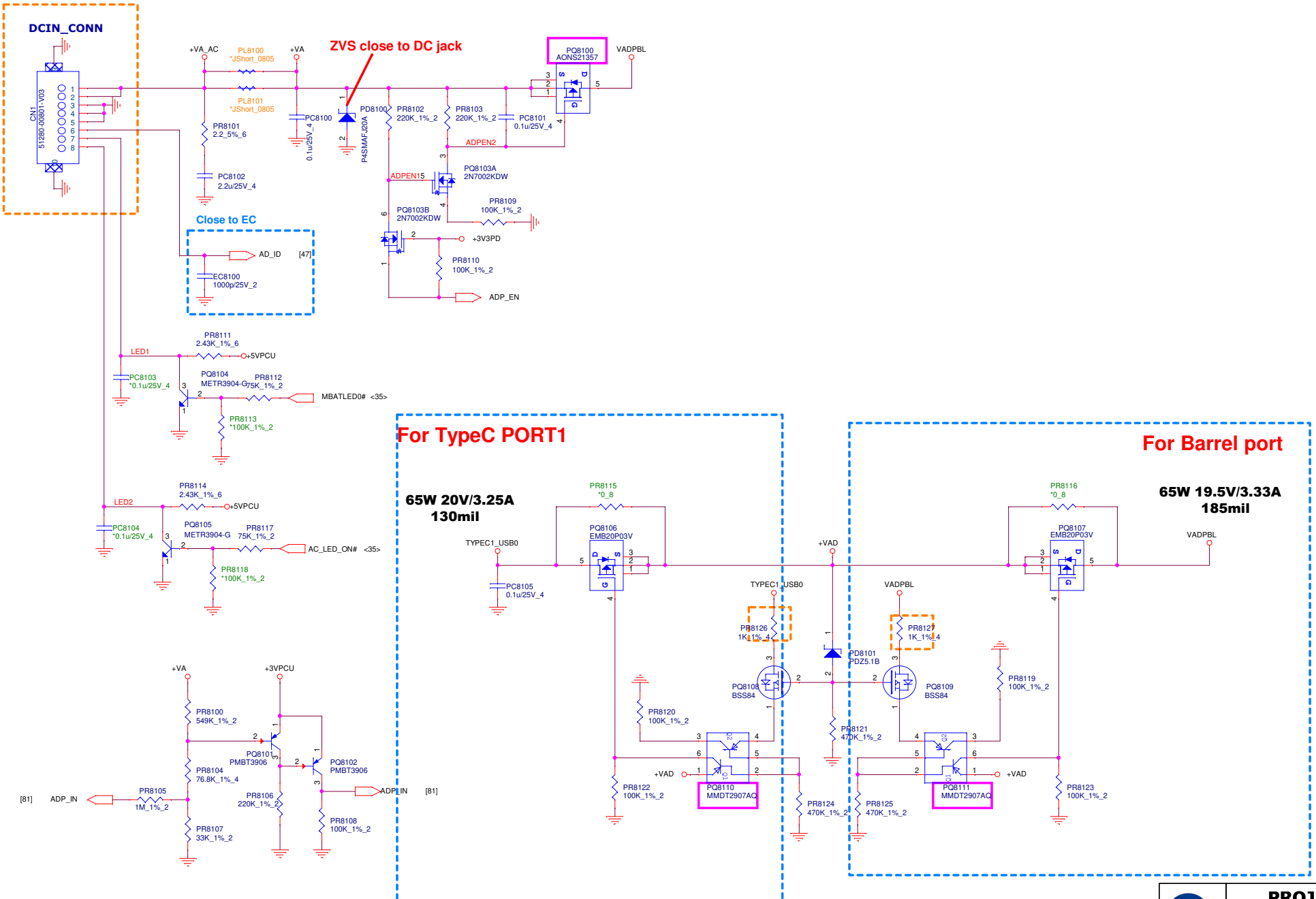
R7801 10K_5%_2



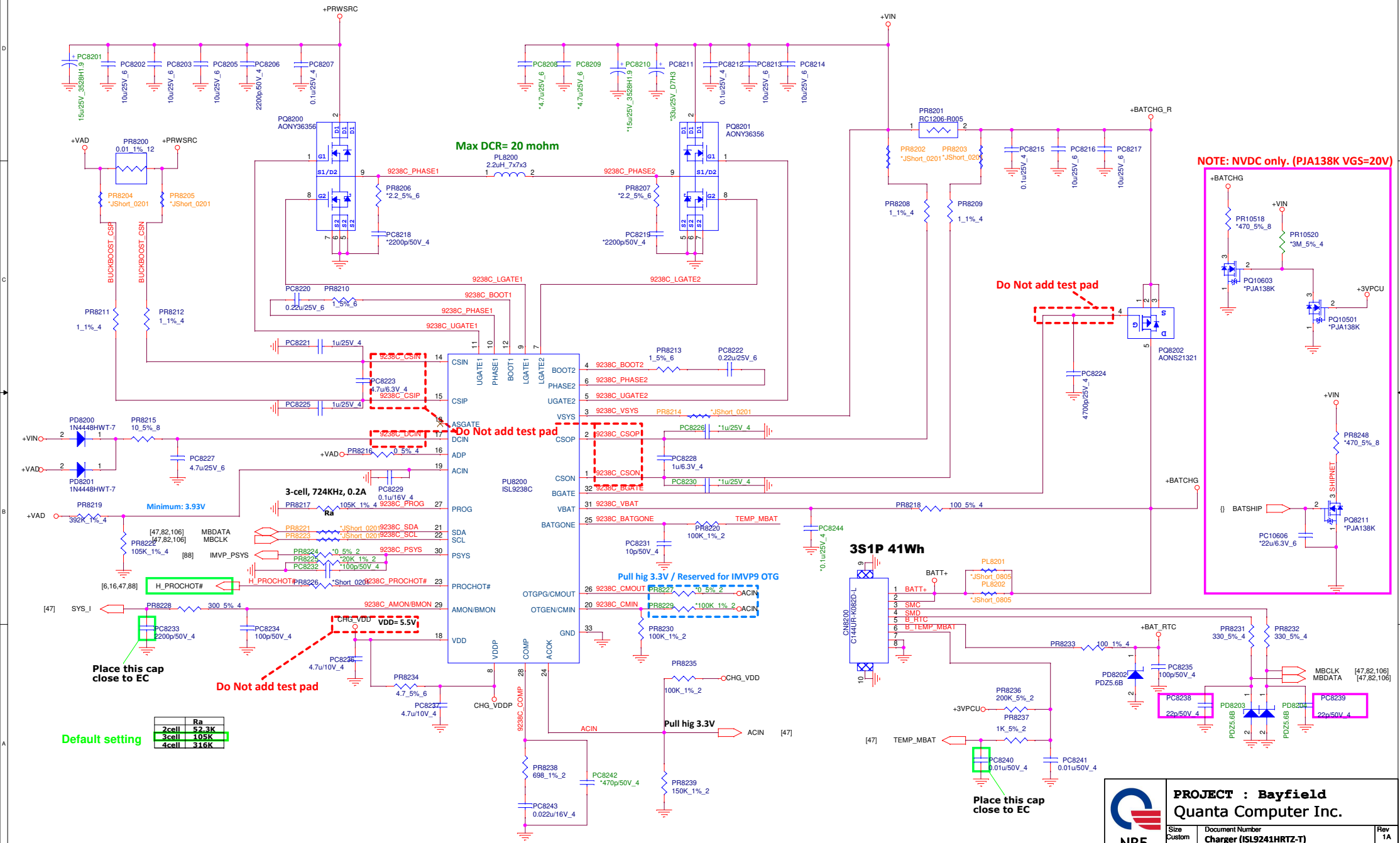


PROJECT : X3A
Quanta Computer Inc.

Size Custom	Document Number TYPEC load switch MOSFET	Rev 1A
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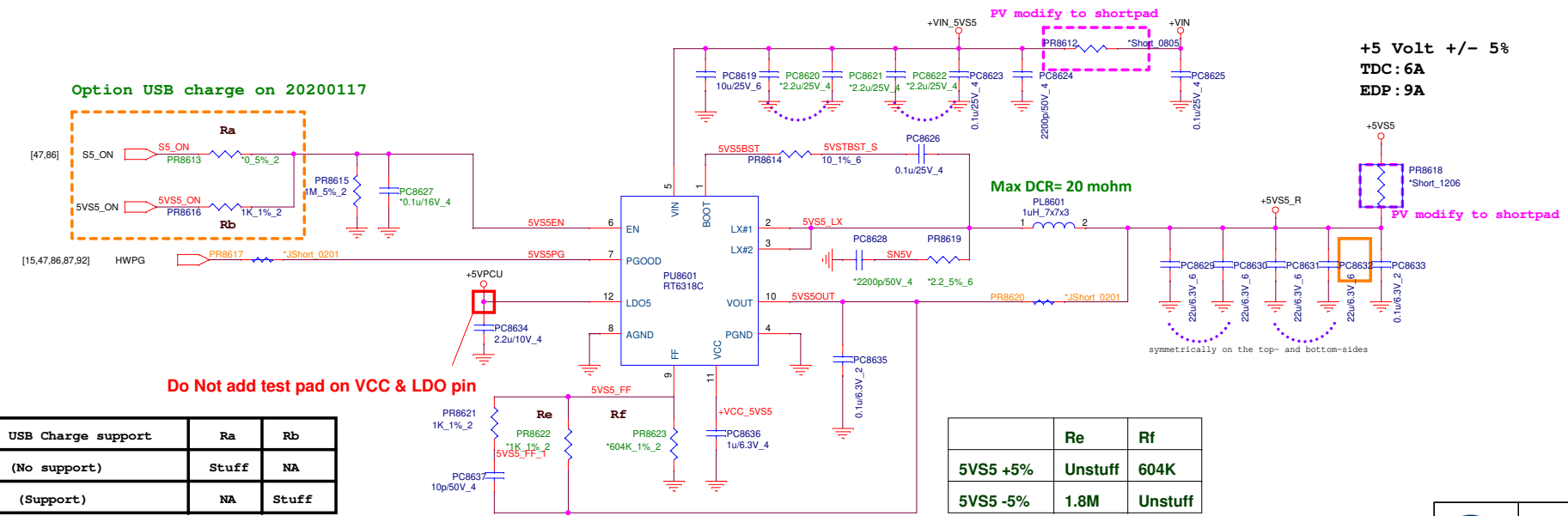
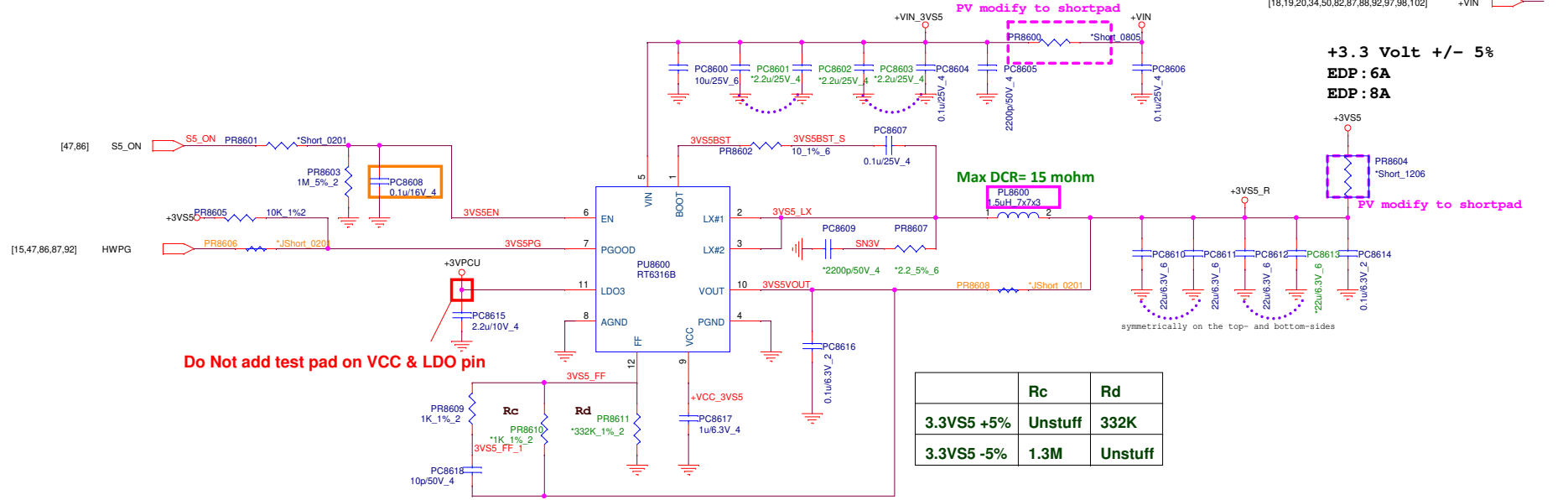


FP6 1125

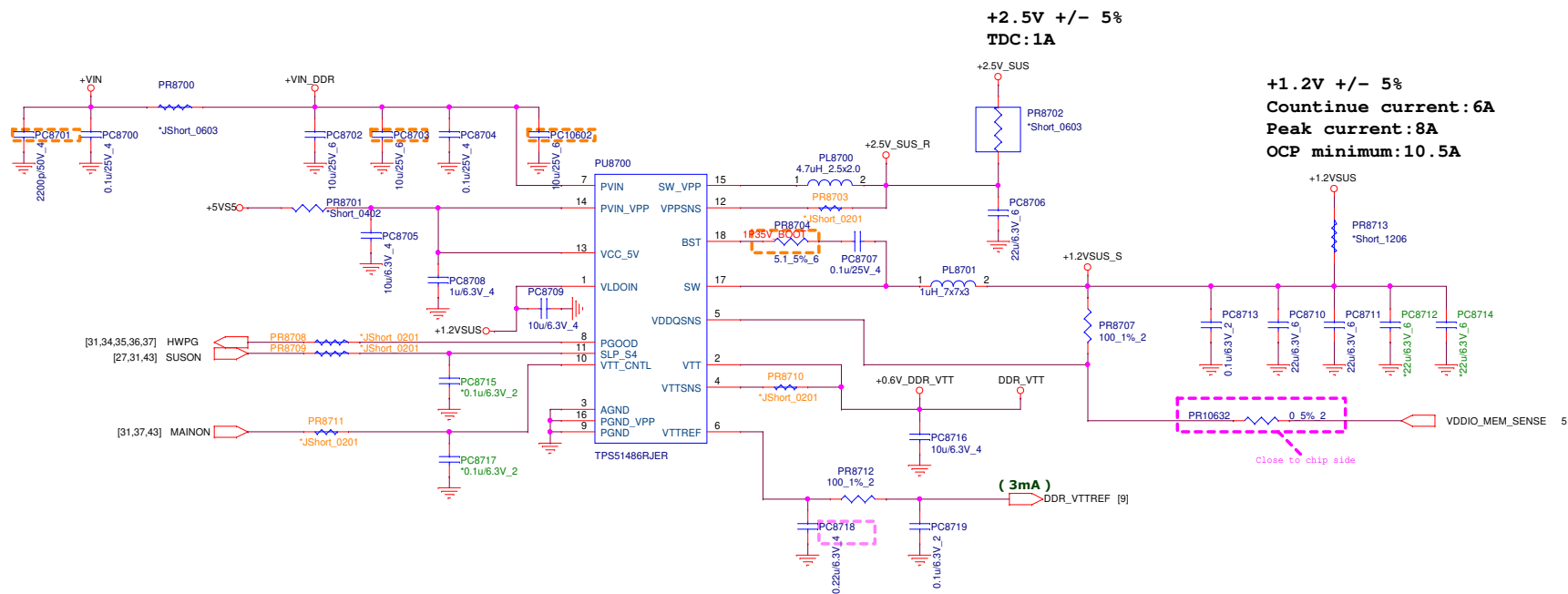


PROJECT : Bayfield
Quanta Computer Inc.

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Charger (ISL9241HRTZ-T)
 Date: Friday, July 10, 2020 Sheet 82 of 106 Rev 1A



USB Charge support	Ra	Rb
(No support)	Stuff	NA
(Support)	NA	Stuff

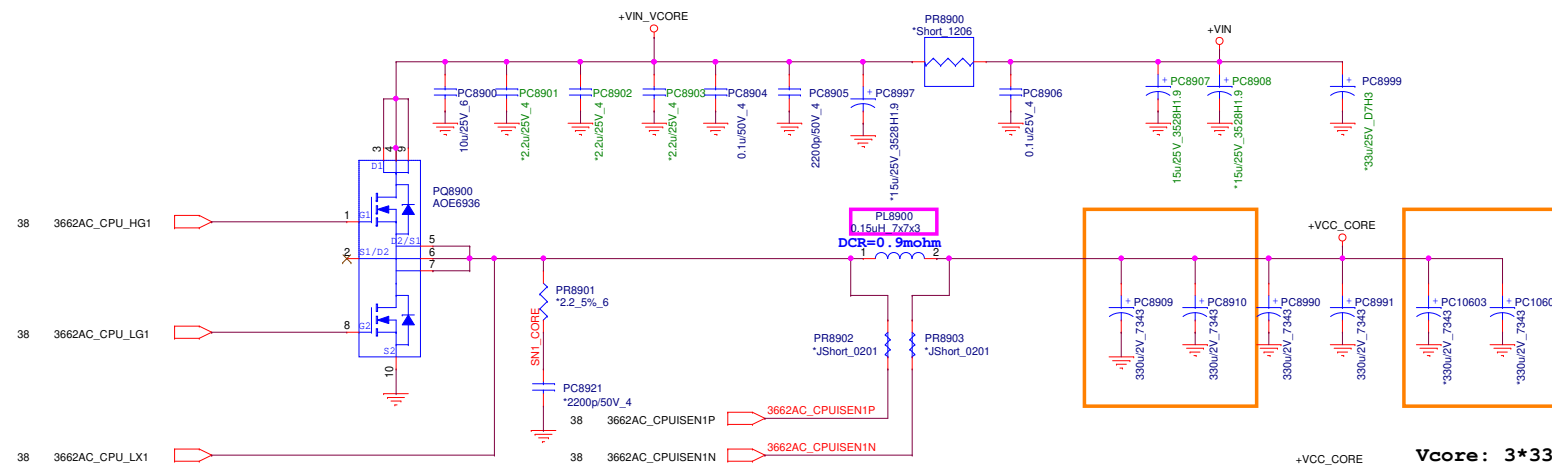




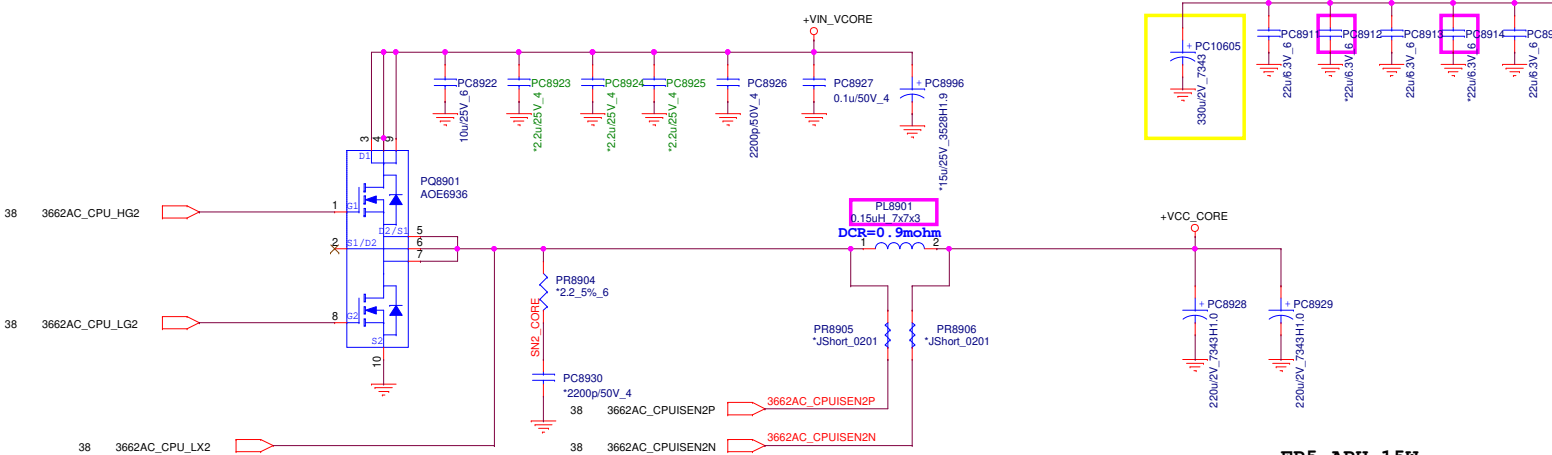
FP5 APU 15W
CPU CORE Volt
 Countinue current:35A
 Peak current:45A
 OCP minimum:80A
 LL= -0.7mV/A

Default setting

FP6 APU 15W
CPU CORE Volt
 Countinue current:33A
 Peak current:50A
 OCP minimum:80A
 LL= -0.7mV/A



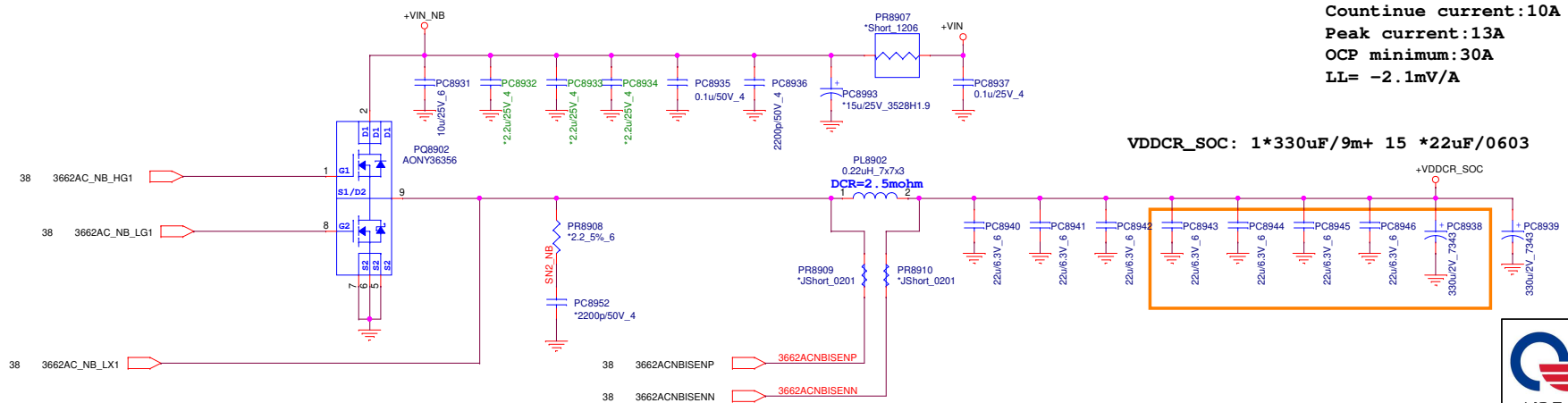
Vcore: 3*330uF/9m+30 *22uF/0603



FP5 APU 15W
VDDCR_SOC Volt
 Countinue current:10A
 Peak current:13A
 OCP minimum:30A
 LL= -2.1mV/A

Default setting

FP6 APU 15W
VDDCR_SOC Volt
 Countinue current:13A
 Peak current:17A
 OCP minimum:30A
 LL= -2.1mV/A



VDDCR_SOC: 1*330uF/9m+ 15 *22uF/0603



PROJECT : G7BF
Quanta Computer Inc.



PROJECT : 400 SERIES
Quanta Computer Inc.

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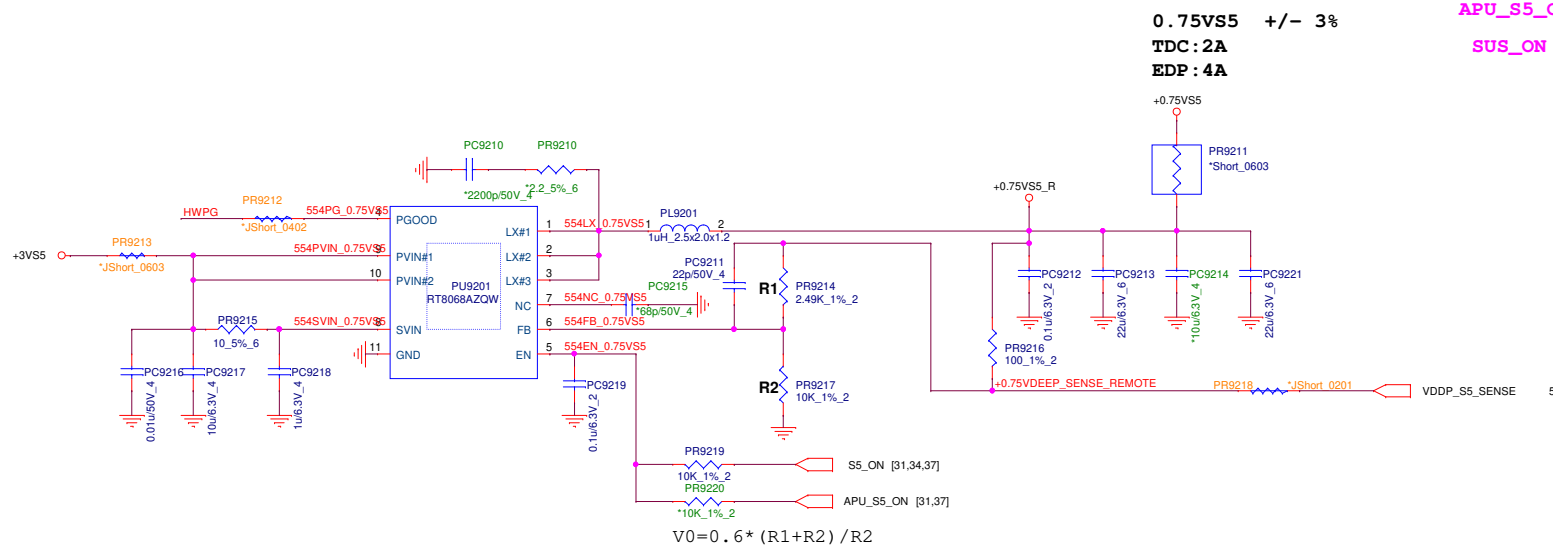
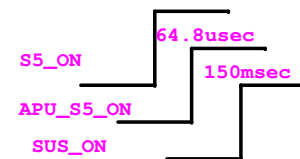
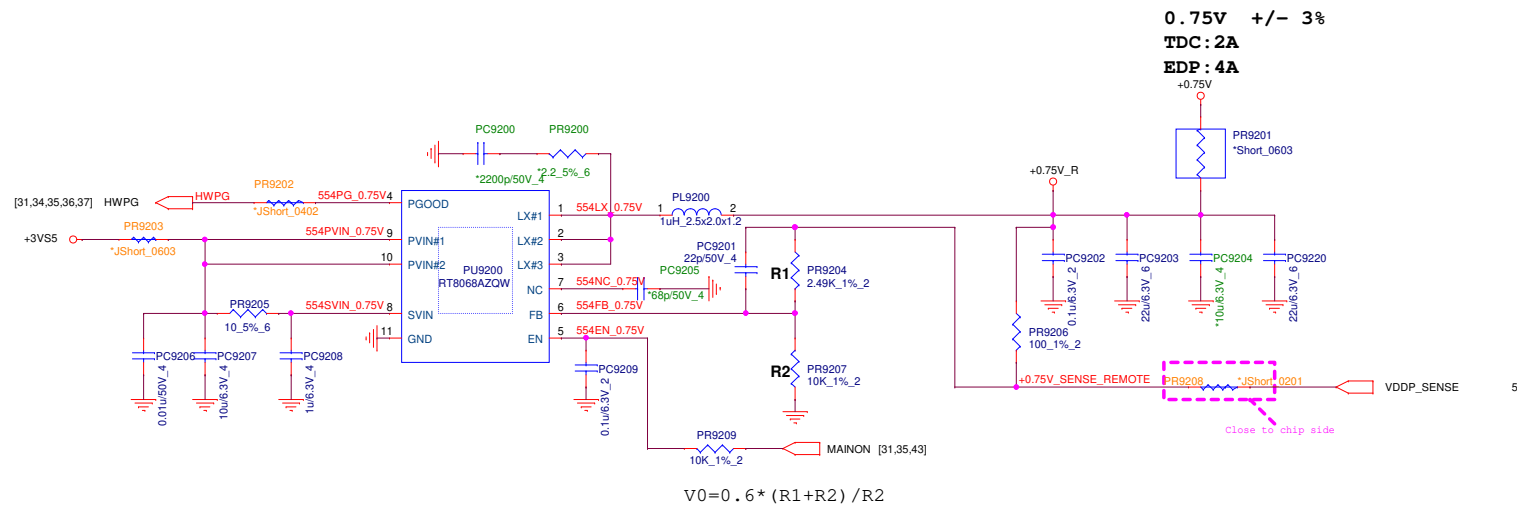
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Quanta Computer Inc.

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Quanta Computer Inc.

Size Custom

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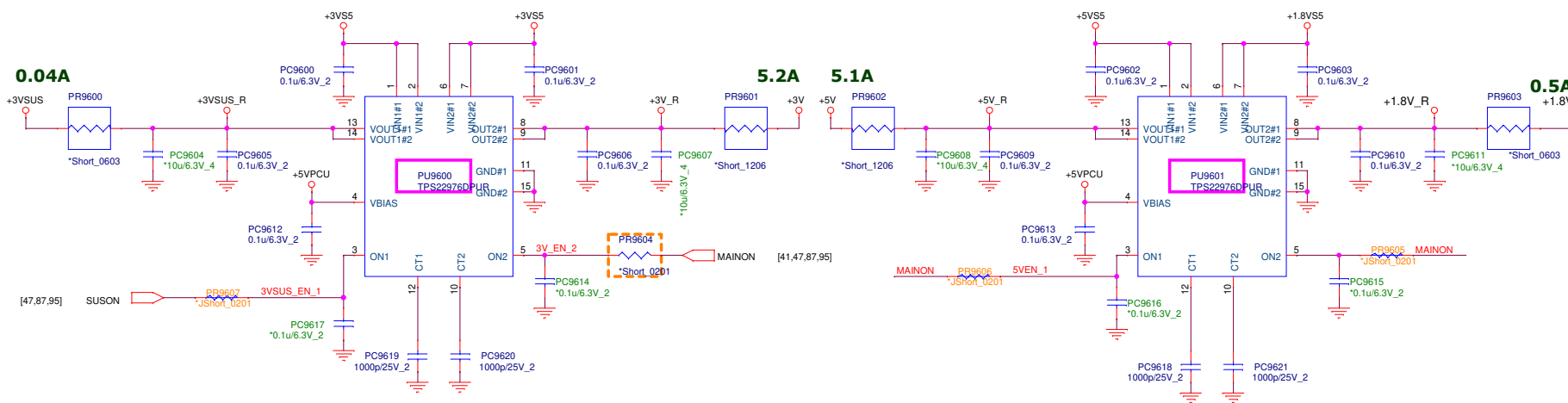


PROJECT : 400 SERIES
Quanta Computer Inc.

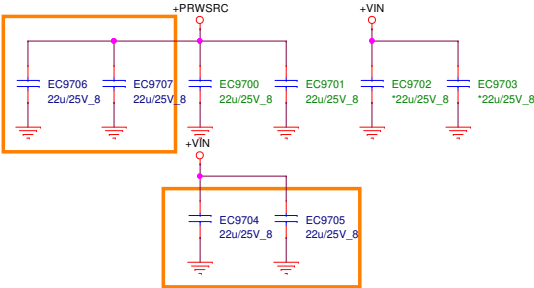
Size Custom

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Reserve for ISEN




A0

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A3

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PROJECT : G7C

Quanta Computer Inc.

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